

6

5

4

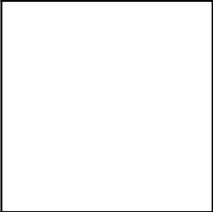
3

2

1

D

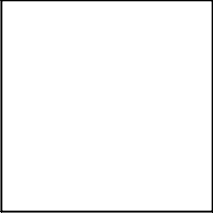
INPUT_BUFFER_CONFIG



SHEET-3

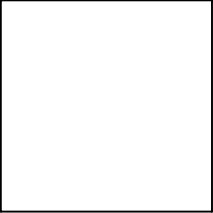
C

IN1_BF



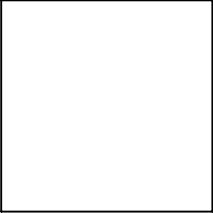
SHEET-4

IN2_BF



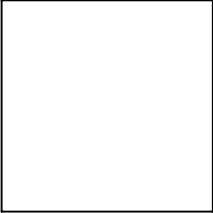
SHEET-5

IN3_BF



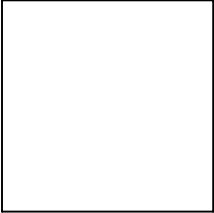
SHEET-6

IN4_BF



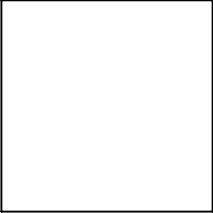
SHEET-7

IN5_BF



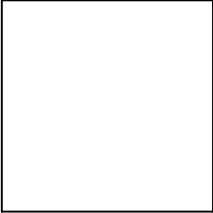
SHEET-9

IN6_BF



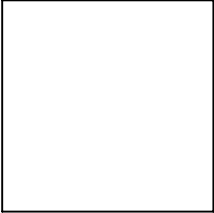
SHEET-10

IN7_BF



SHEET-11

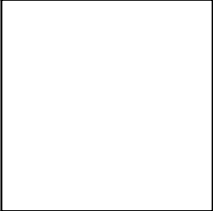
IN8_BF



SHEET-12

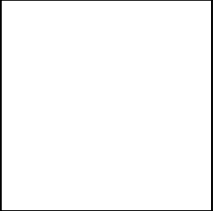
B

IN1-4_BF_MISC



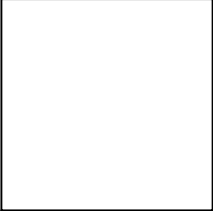
SHEET-8

IN5-8_BF_MISC



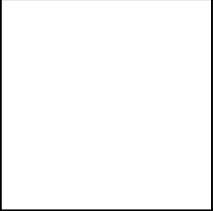
SHEET-13

INPUT_FILTERS_INTERNAL



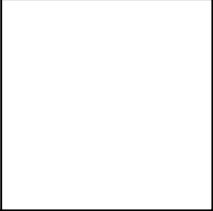
SHEET-14

CS530X_IN1-4



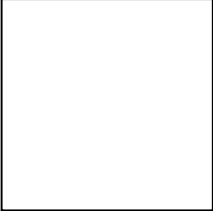
SHEET-15

CS530X_IN5-8



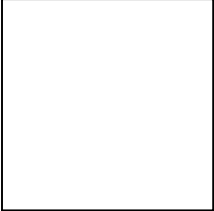
SHEET-16

CS530X



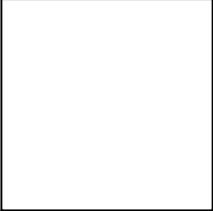
SHEET-17

CS530X_MISC



SHEET-18

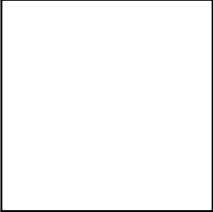
CLK_GEN



SHEET-19

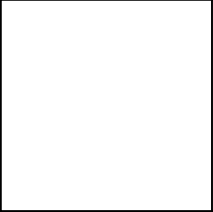
A

DC_CONN1



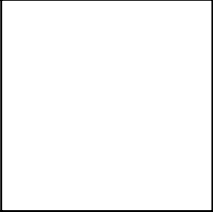
SHEET-21

DC_CONN2



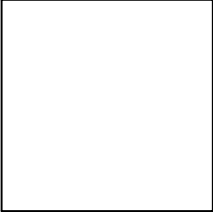
SHEET-22

MISC



SHEET-20

TEST_POINTS



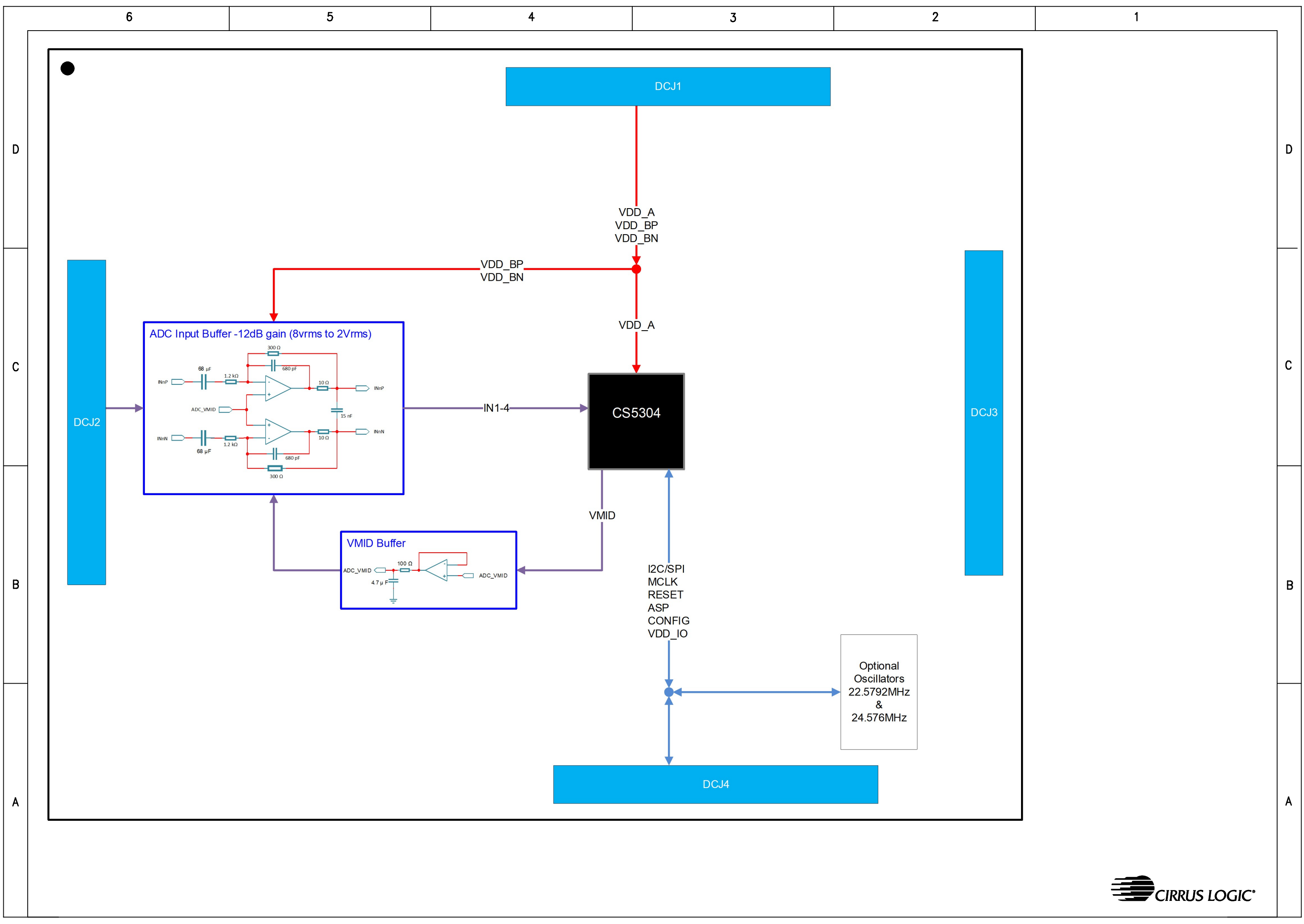
SHEET-23

D

C

B

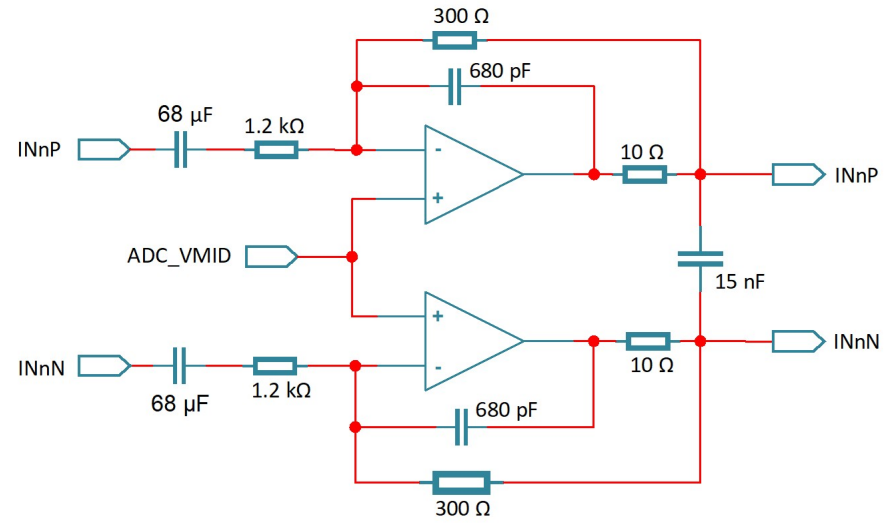
A



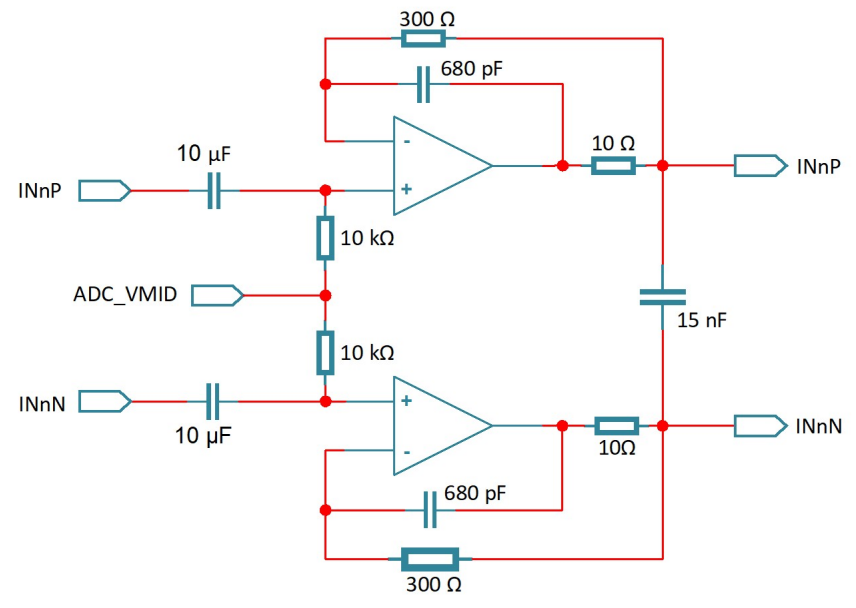
Input Buffer/Filter Options

Differential Input Circuits

Inverting

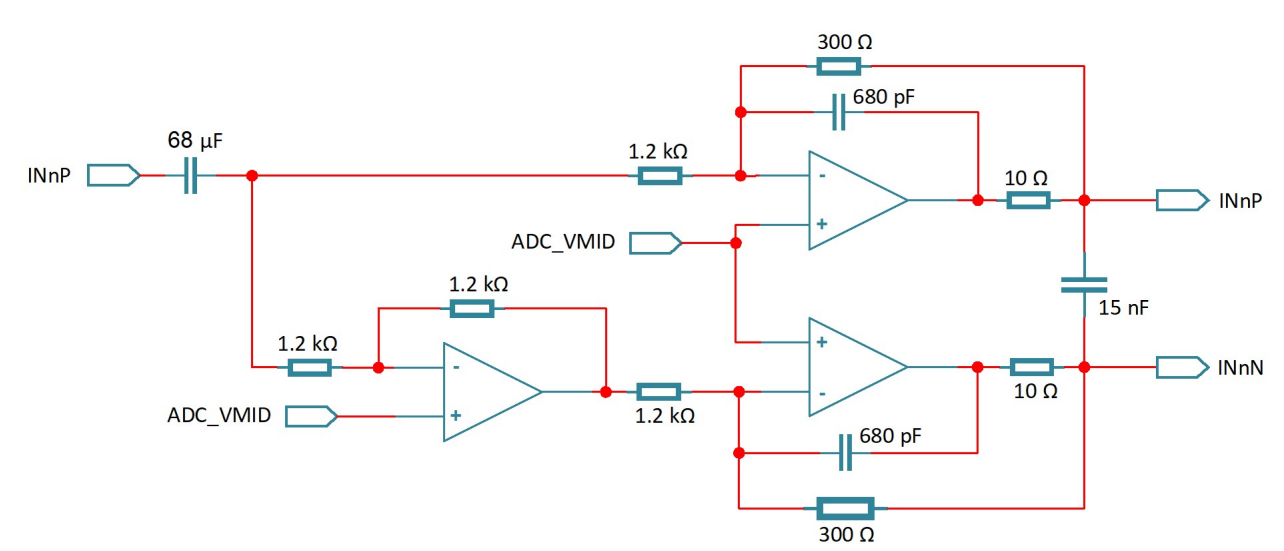


Non-Inverting (ADC VMID must be buffered in this use case)

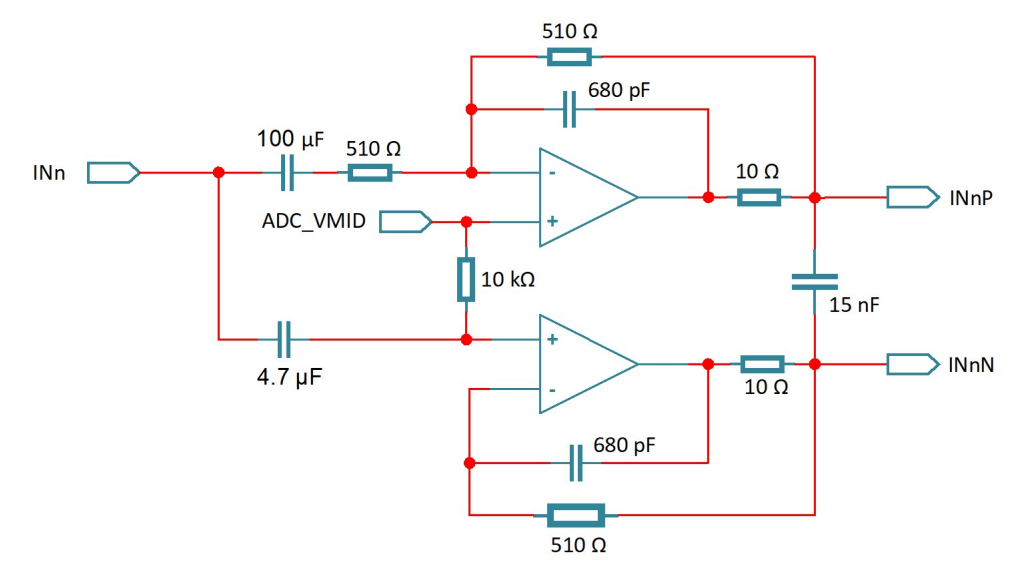


Single Ended Input Circuits

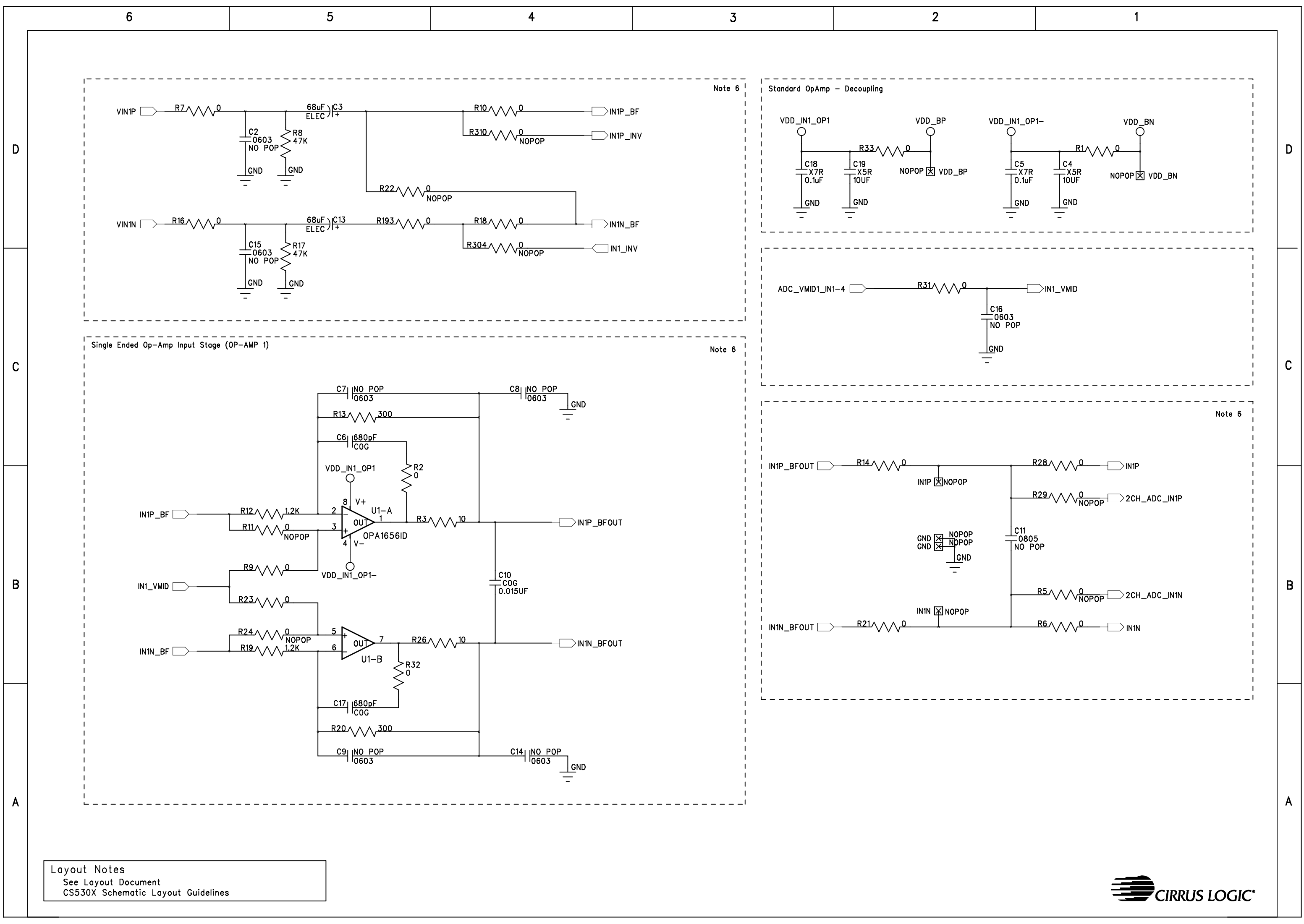
Single Ended to Differential



Single Ended to Differential (ADC VMID must be buffered in this use case)

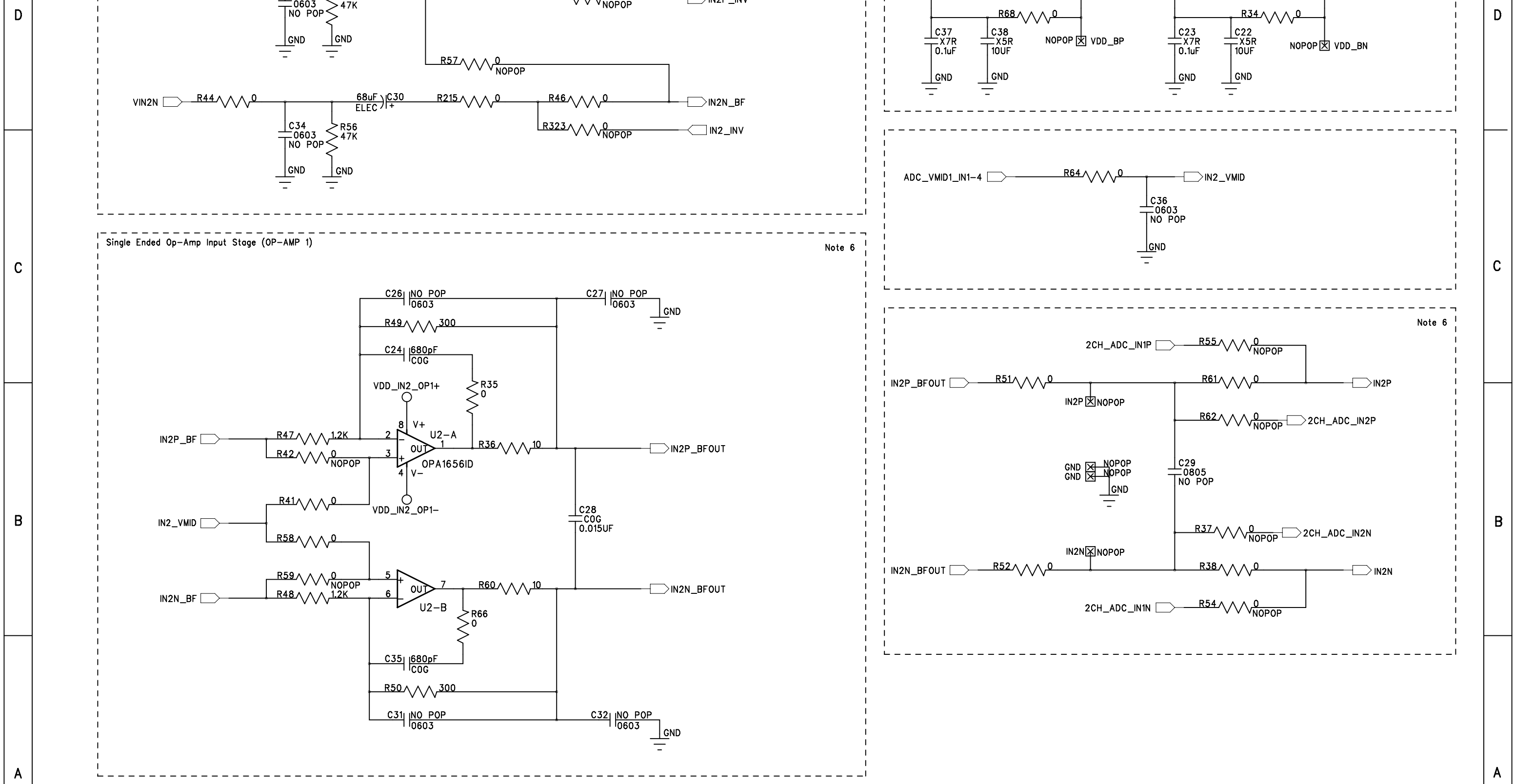


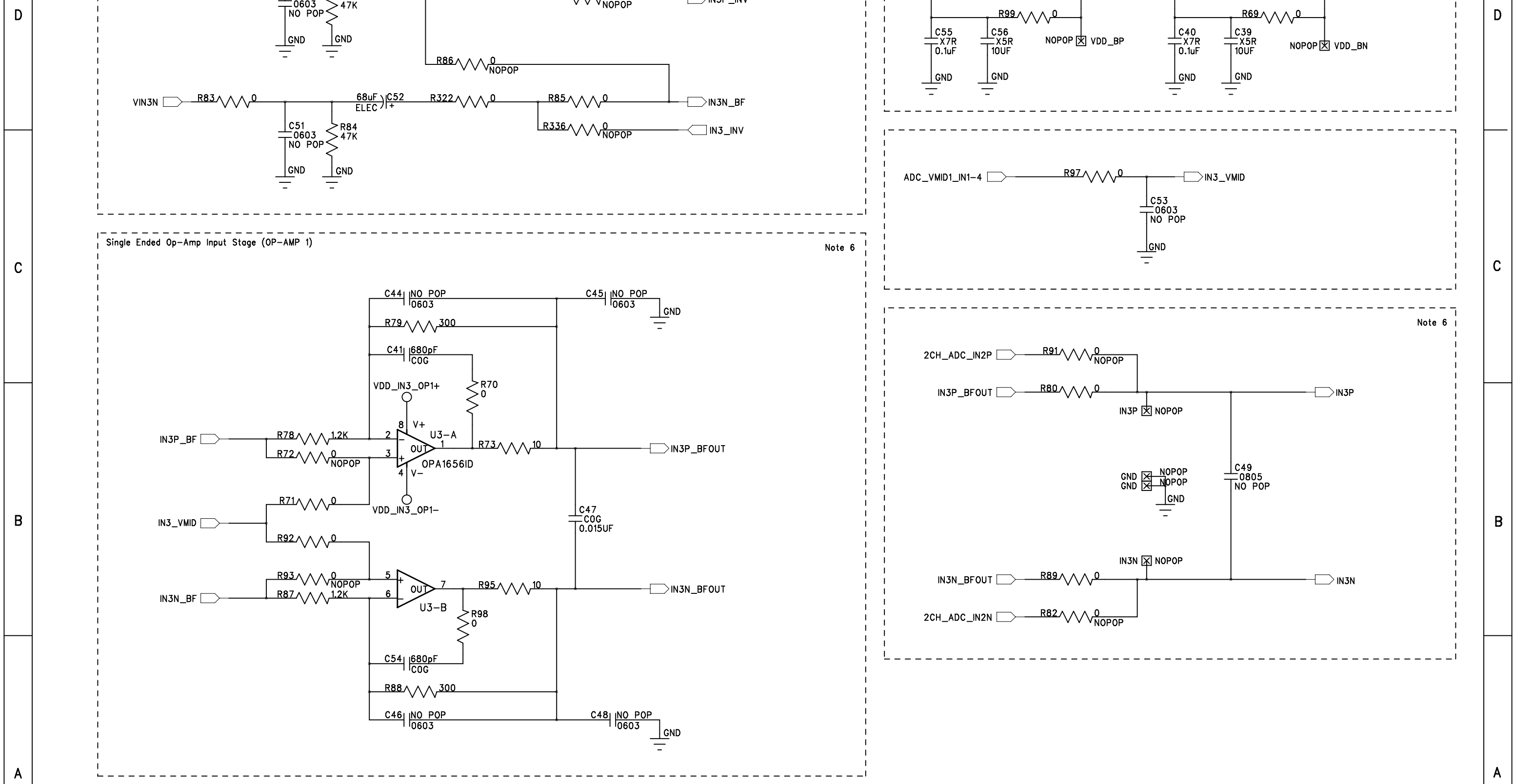
Layout Notes
See Layout Document
CS530X Schematic Layout Guidelines



Layout Notes
See Layout Document
CS530X Schematic Layout Guidelines

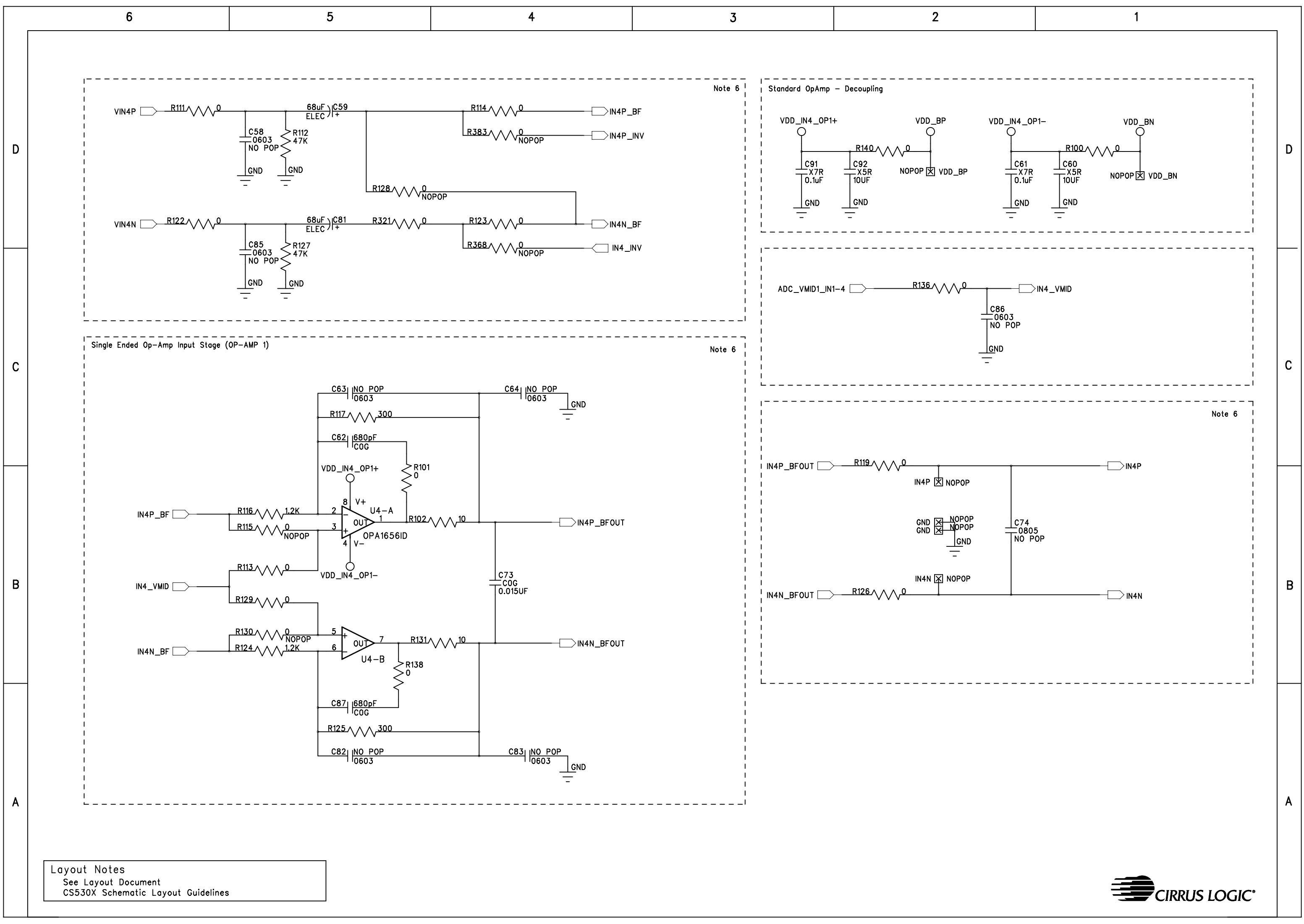






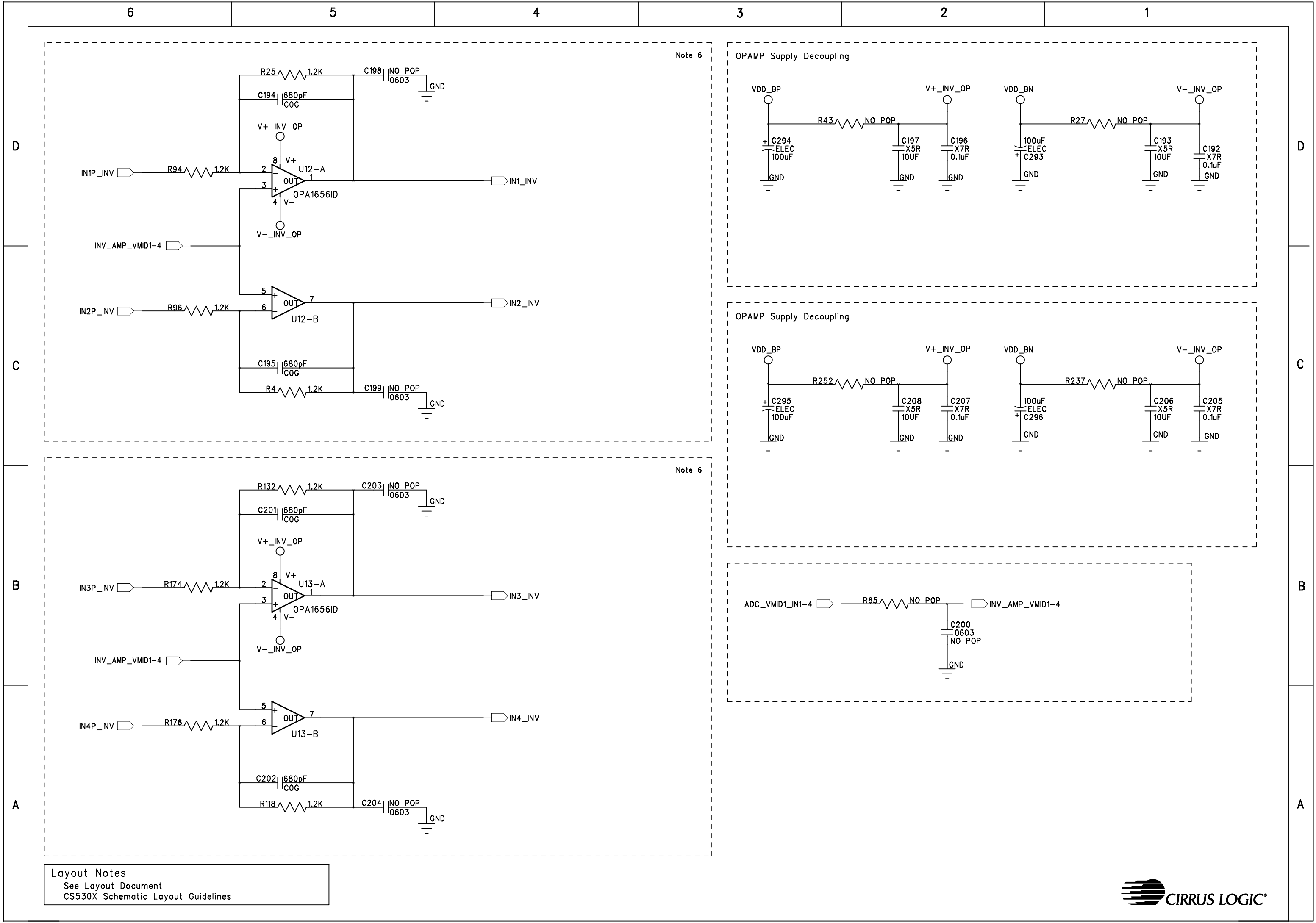
Layout Notes
See Layout Document
CS530X Schematic Layout Guidelines

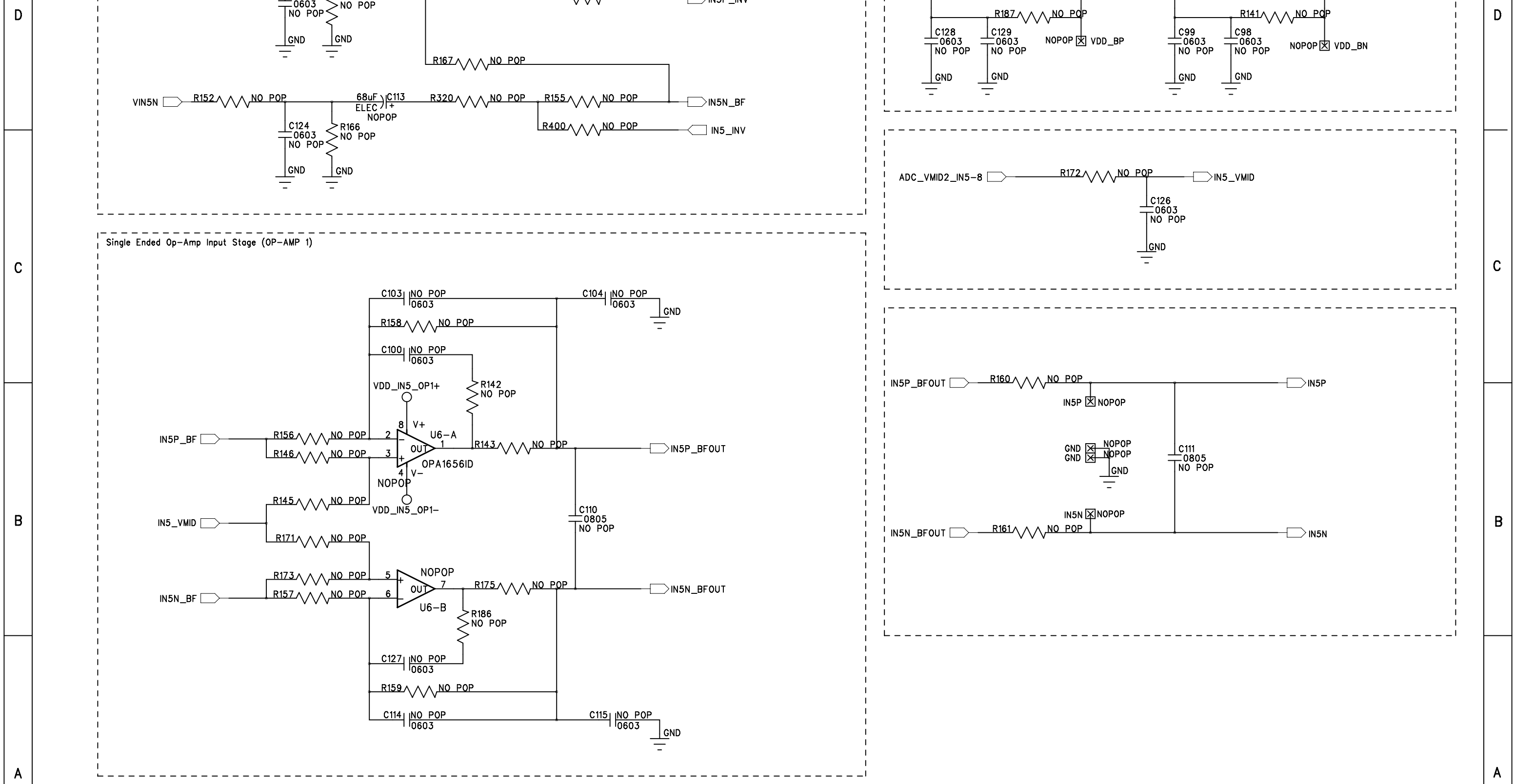


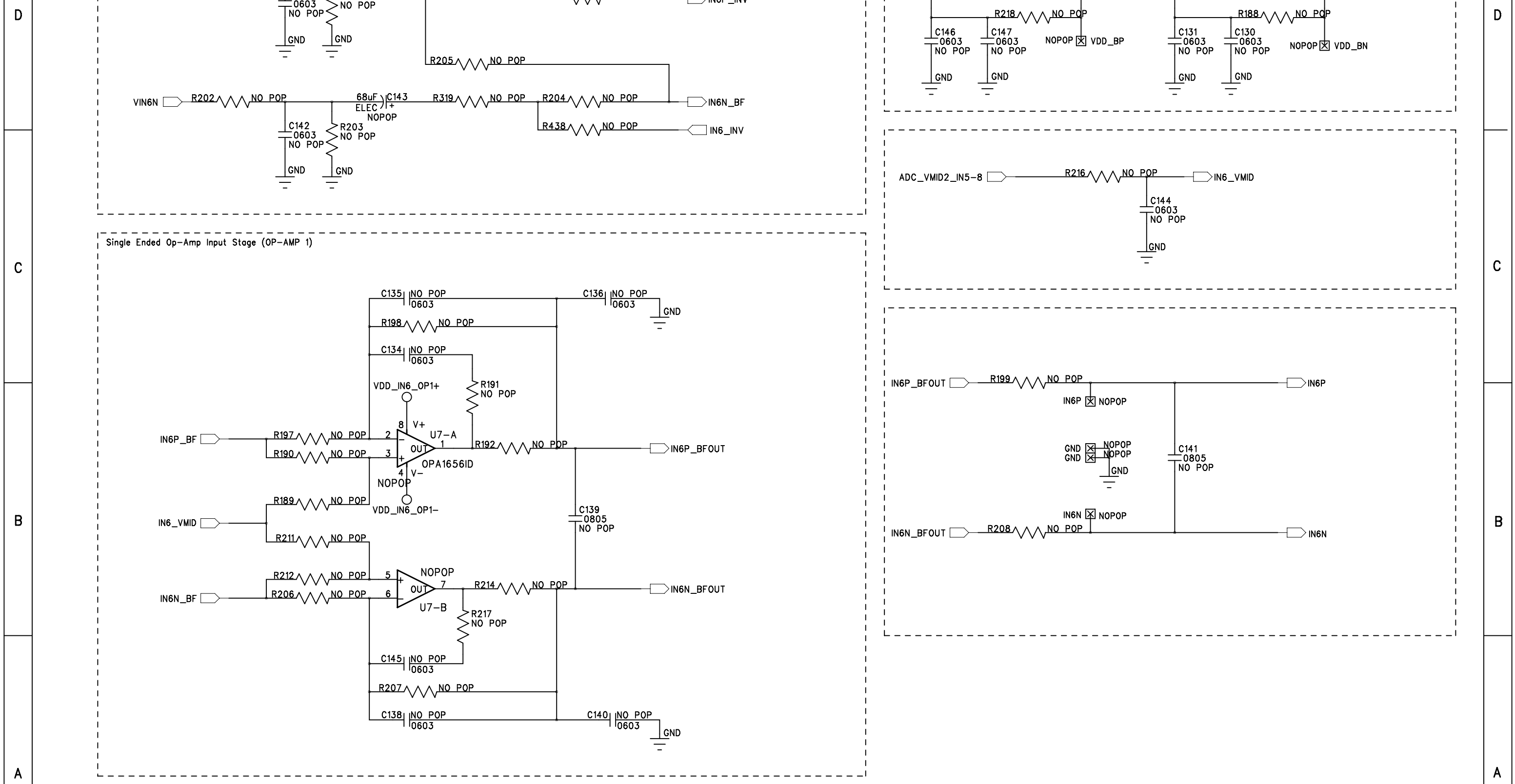


Layout Notes
See Layout Document
CS530X Schematic Layout Guidelines

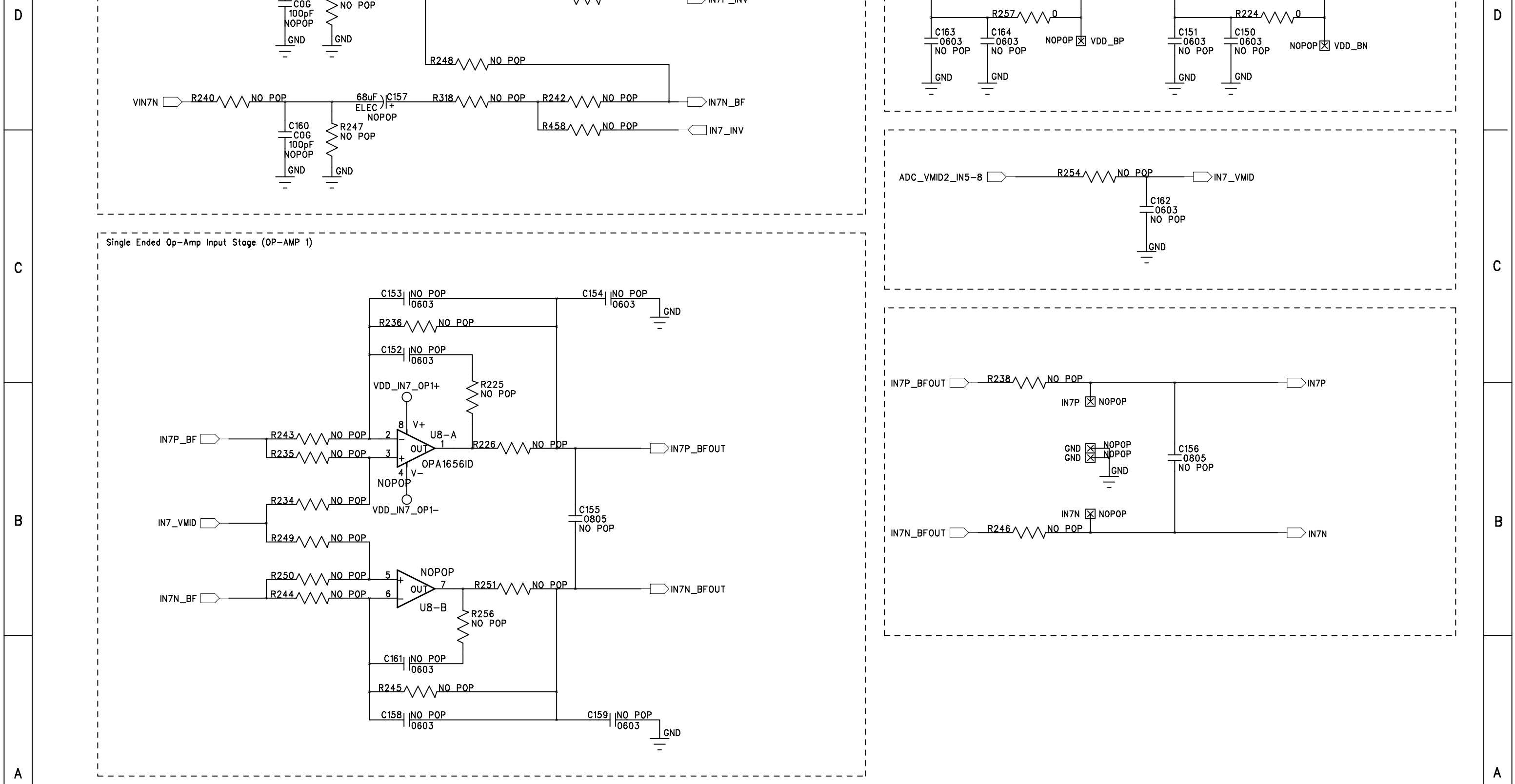


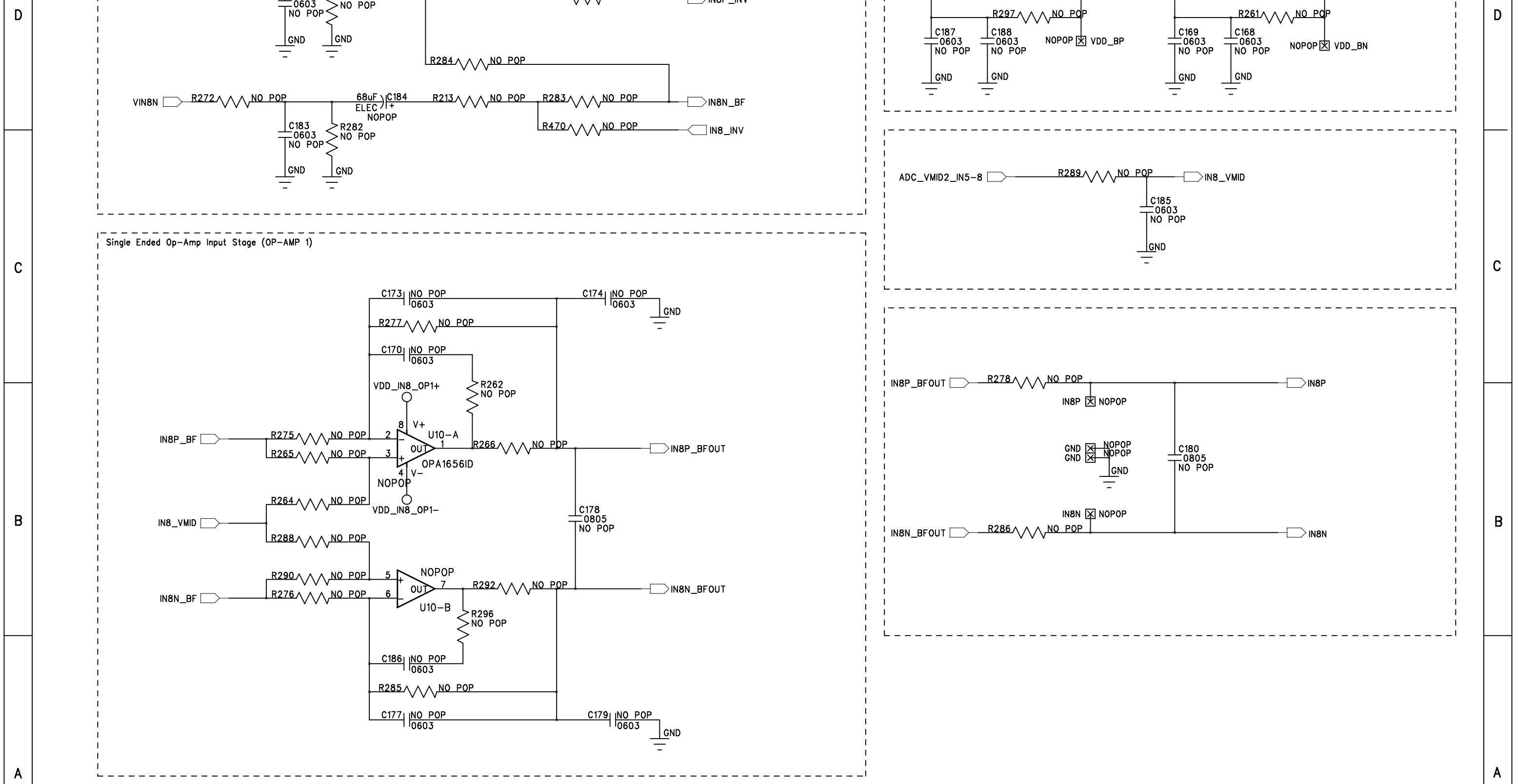


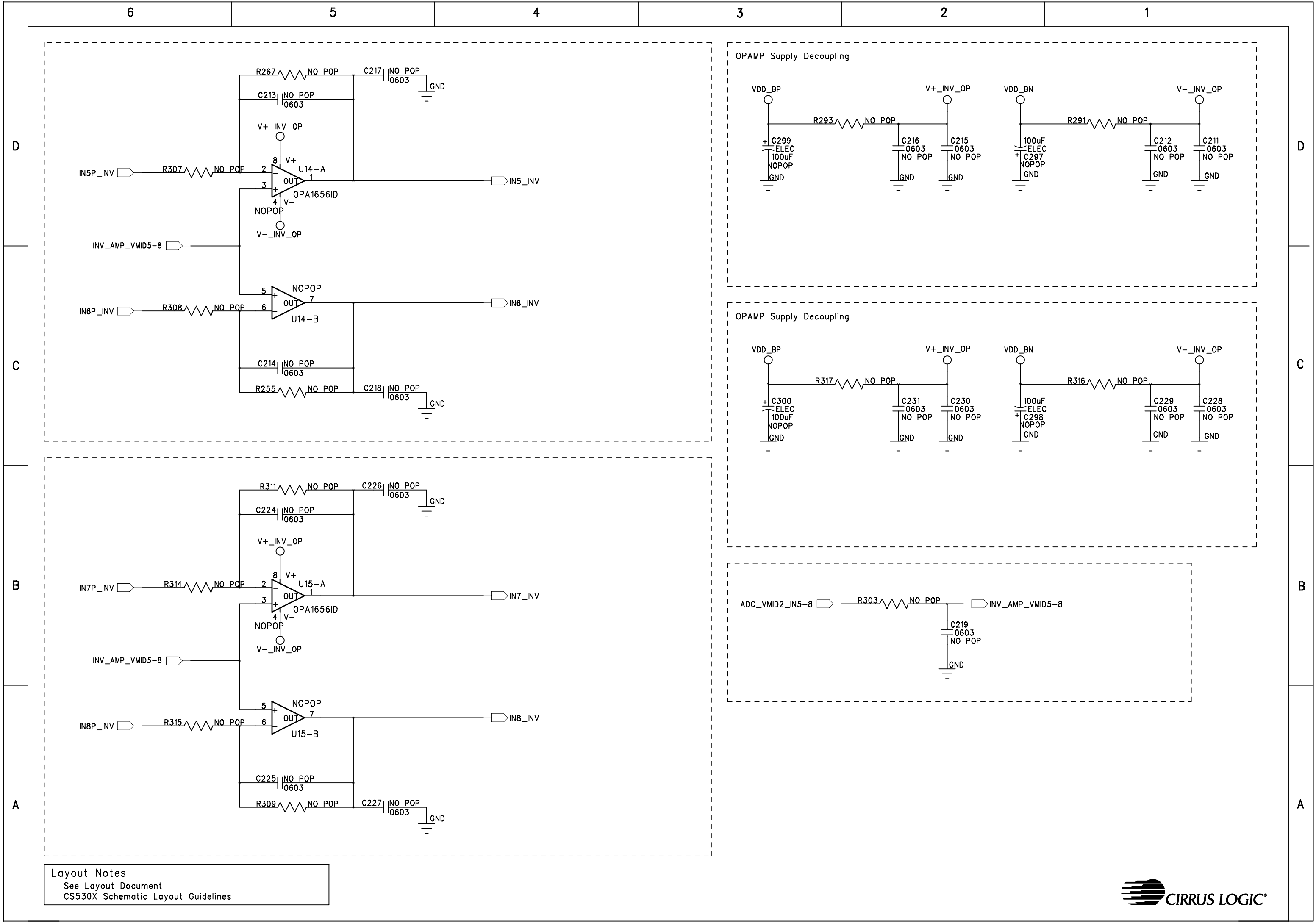




Layout Notes
See Layout Document
CS530X Schematic Layout Guidelines



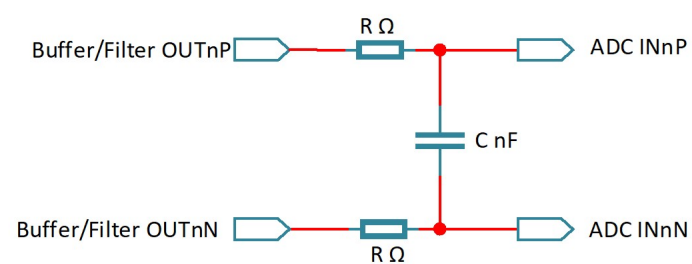




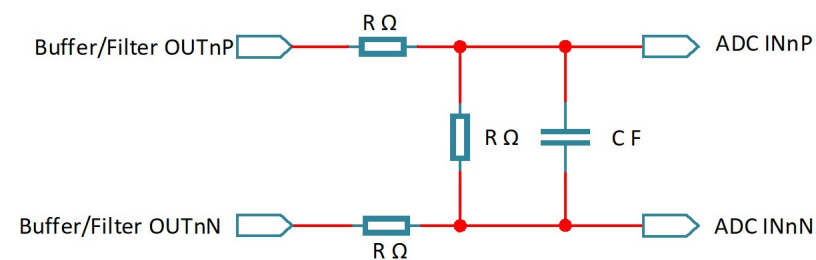
Layout Notes
See Layout Document
CS530X Schematic Layout Guidelines

Additional Filters After Buffer Filter

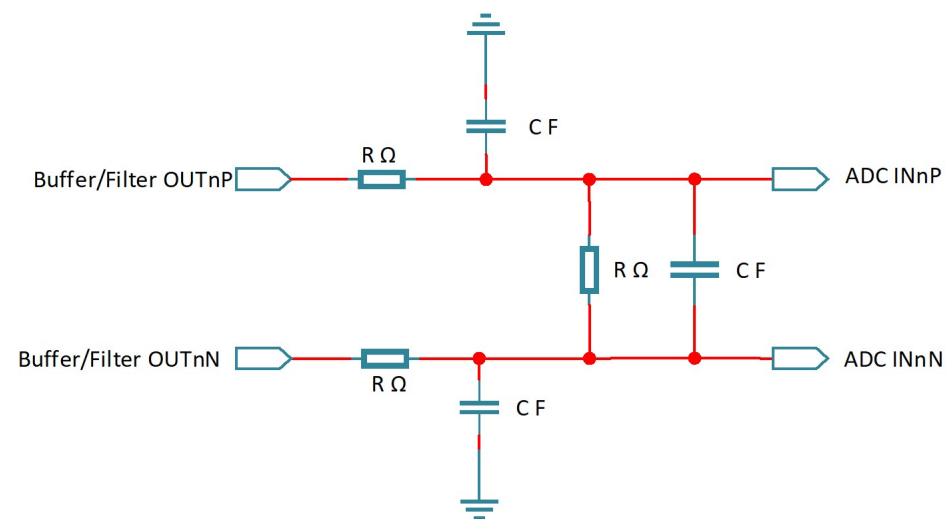
Input Filter After Buffer Filter



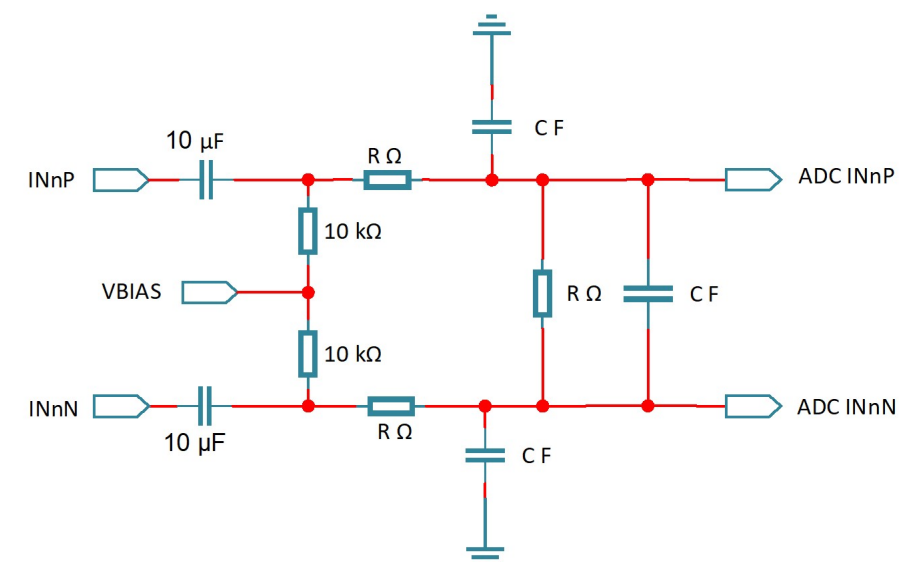
Input Filter After Buffer Filter



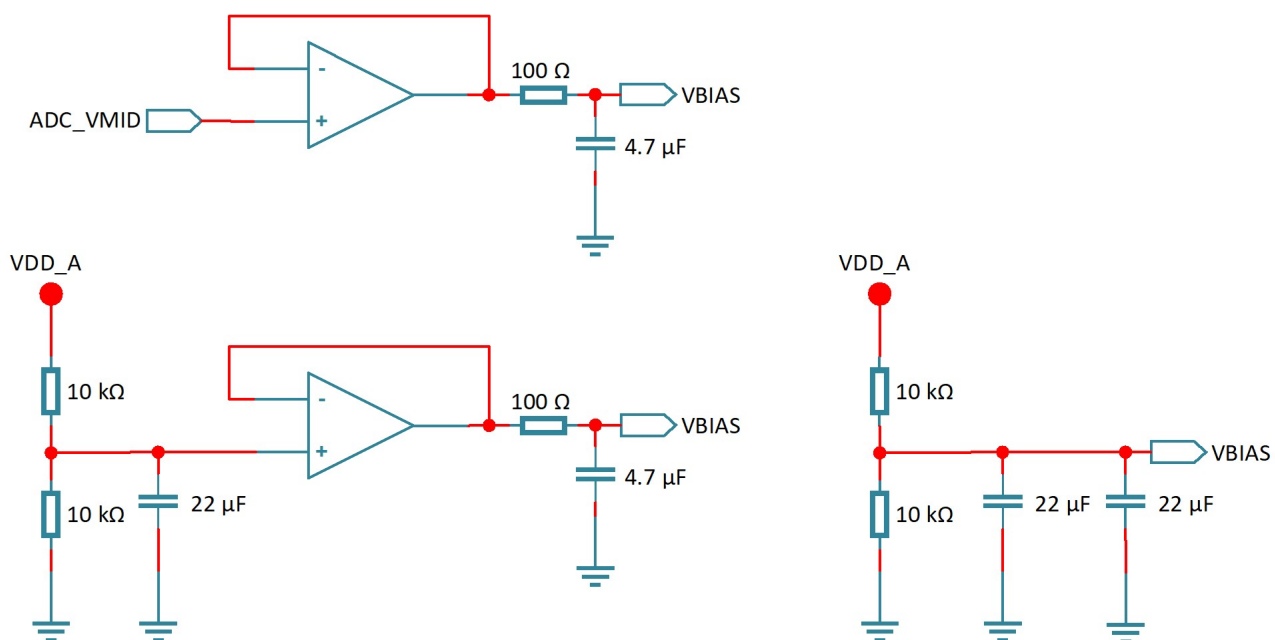
Input Filter After Buffer Filter

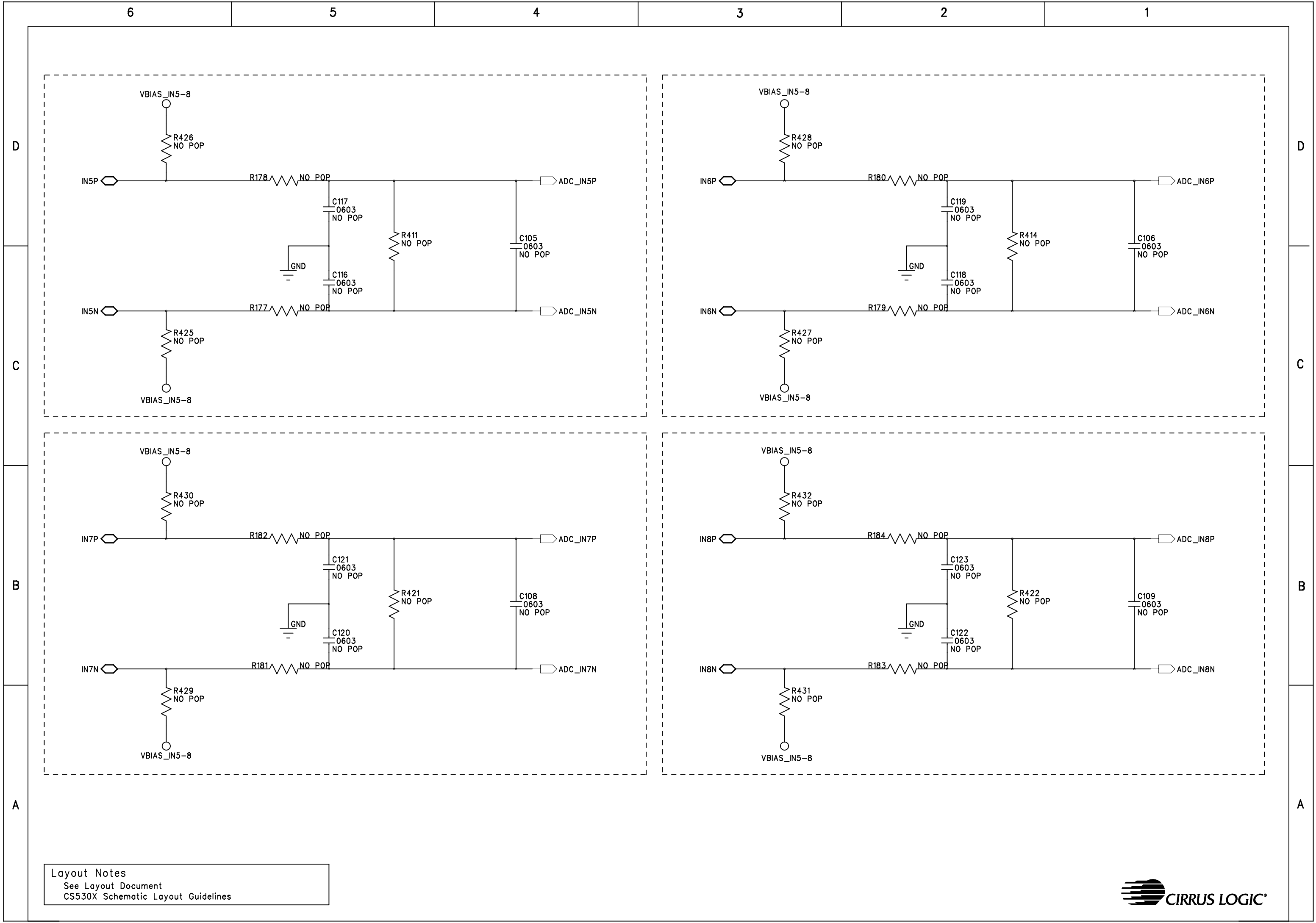


Passive Filter Input (No Buffer/Filter)



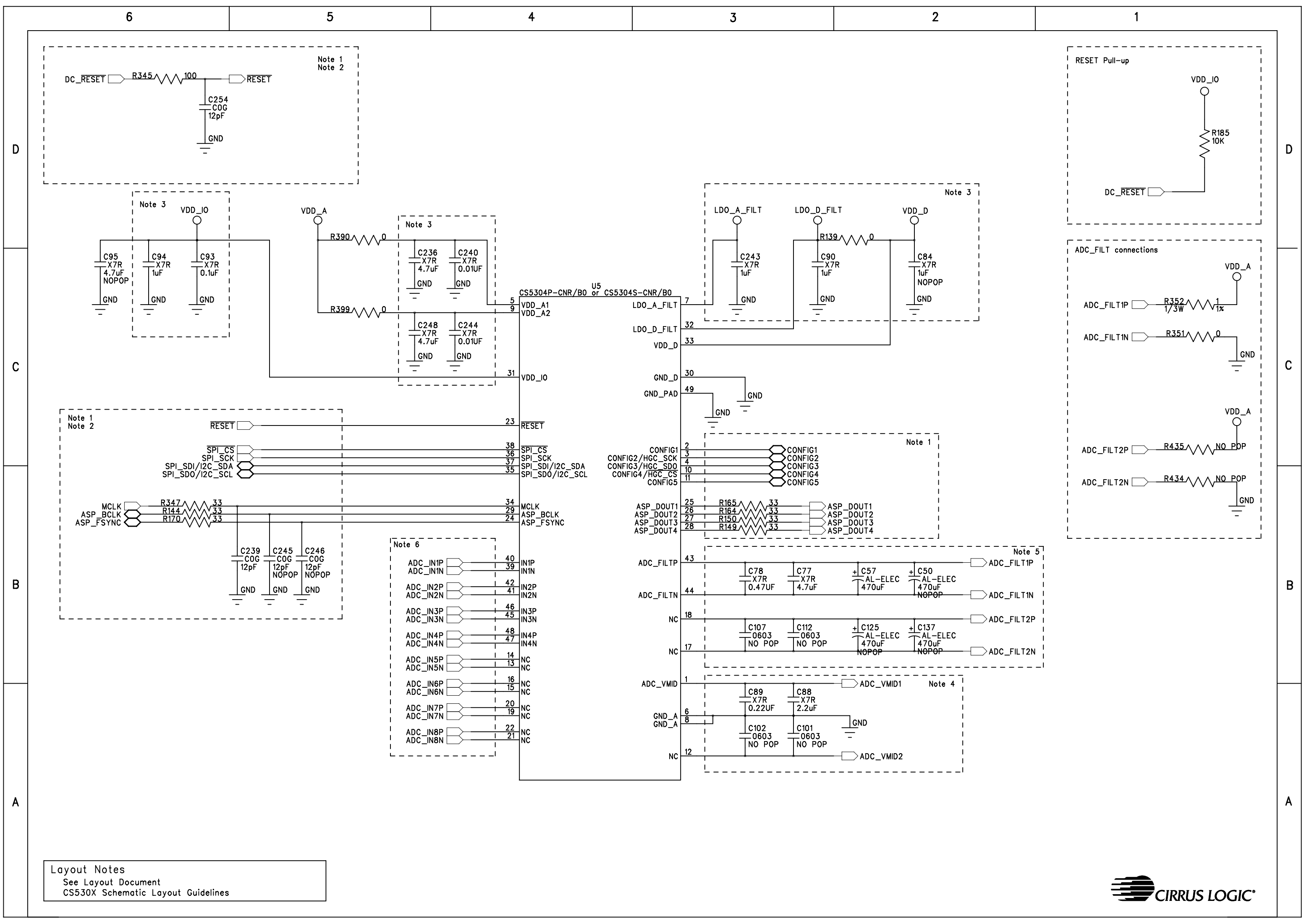
VBIAS Generation options



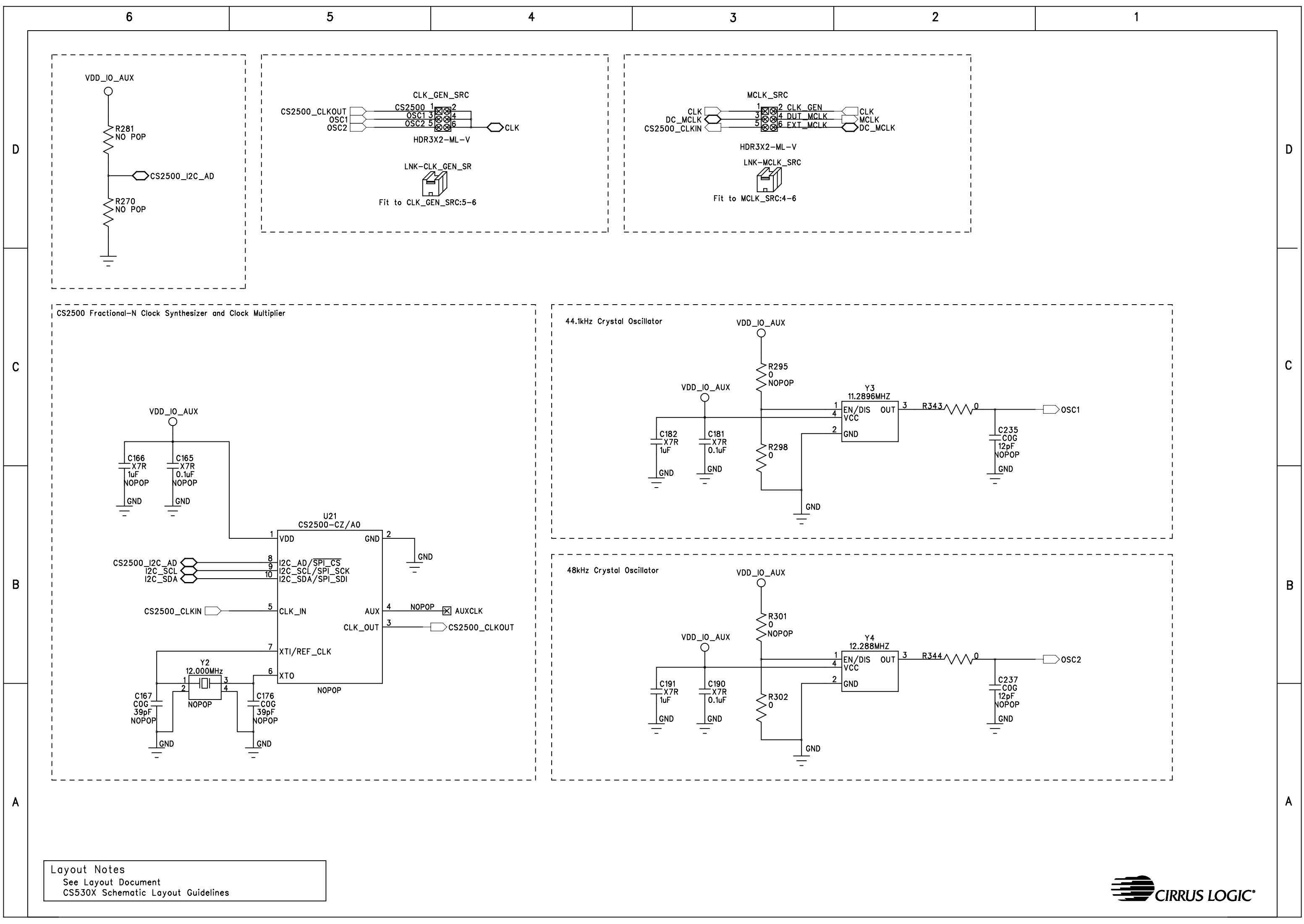


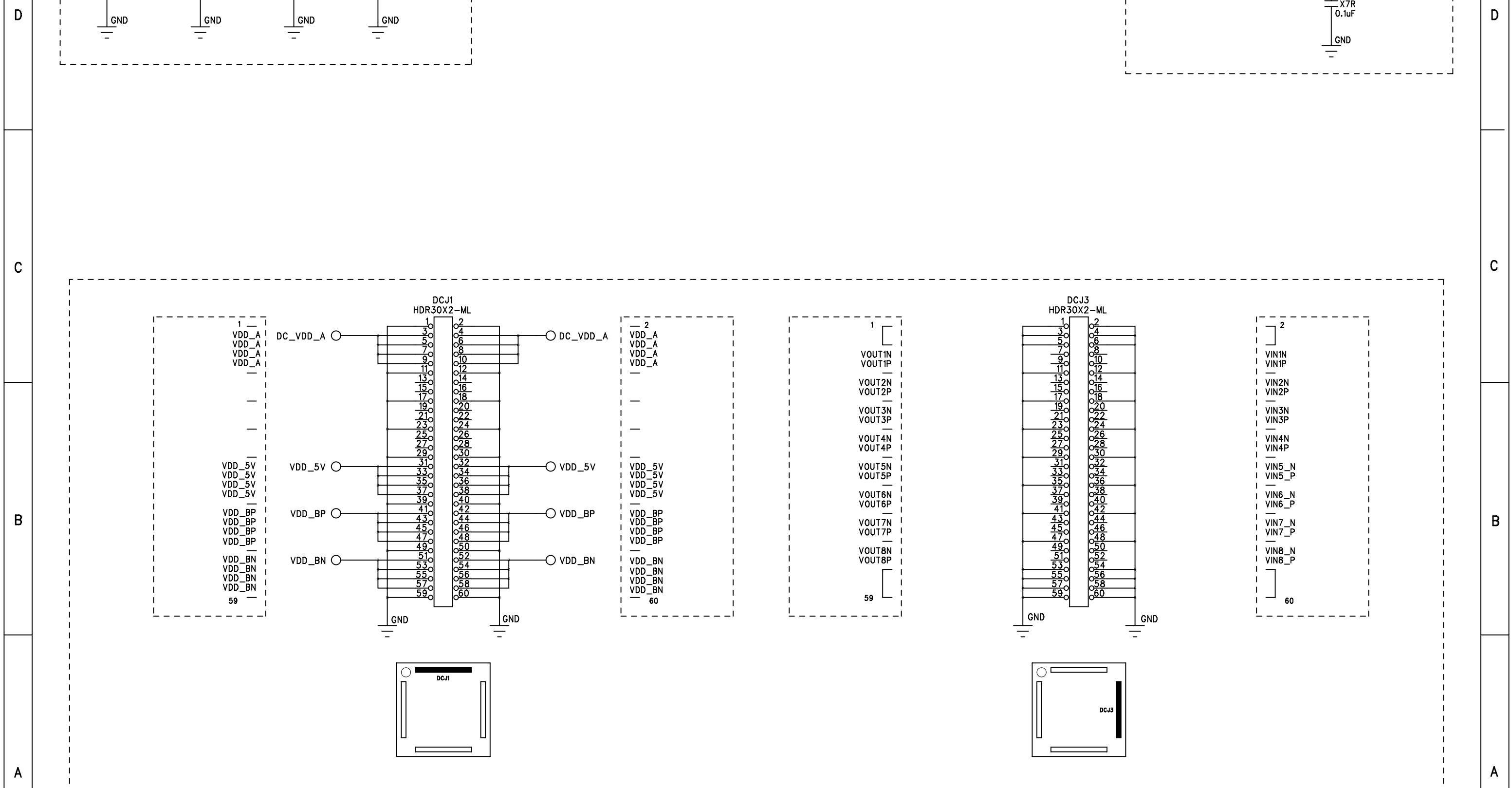
Layout Notes
See Layout Document
CS530X Schematic Layout Guidelines

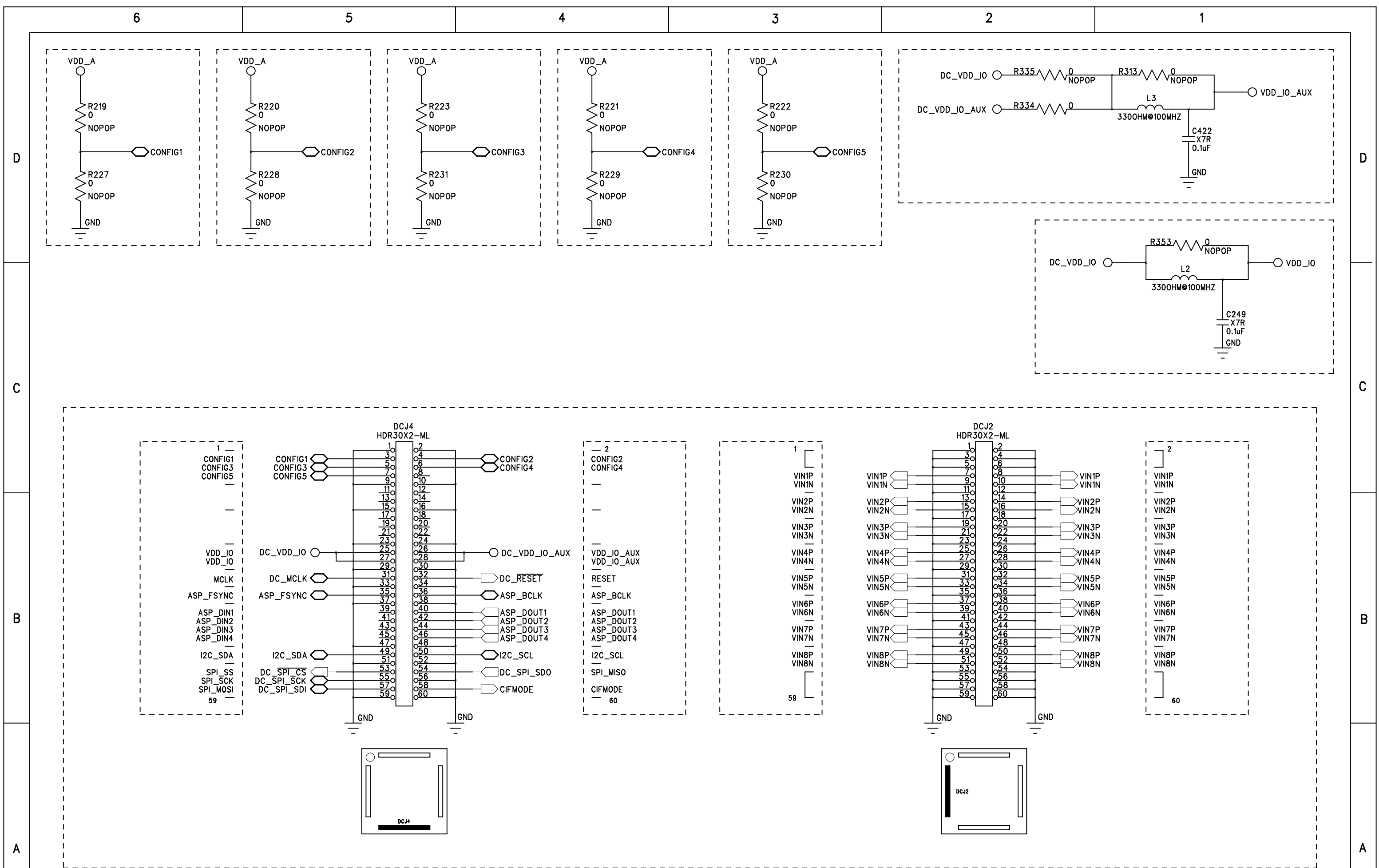




Layout Notes
See Layout Document
CS530X Schematic Layout Guidelines







Layout Notes
See Layout Document
CS530X Schematic Layout Guidelines

