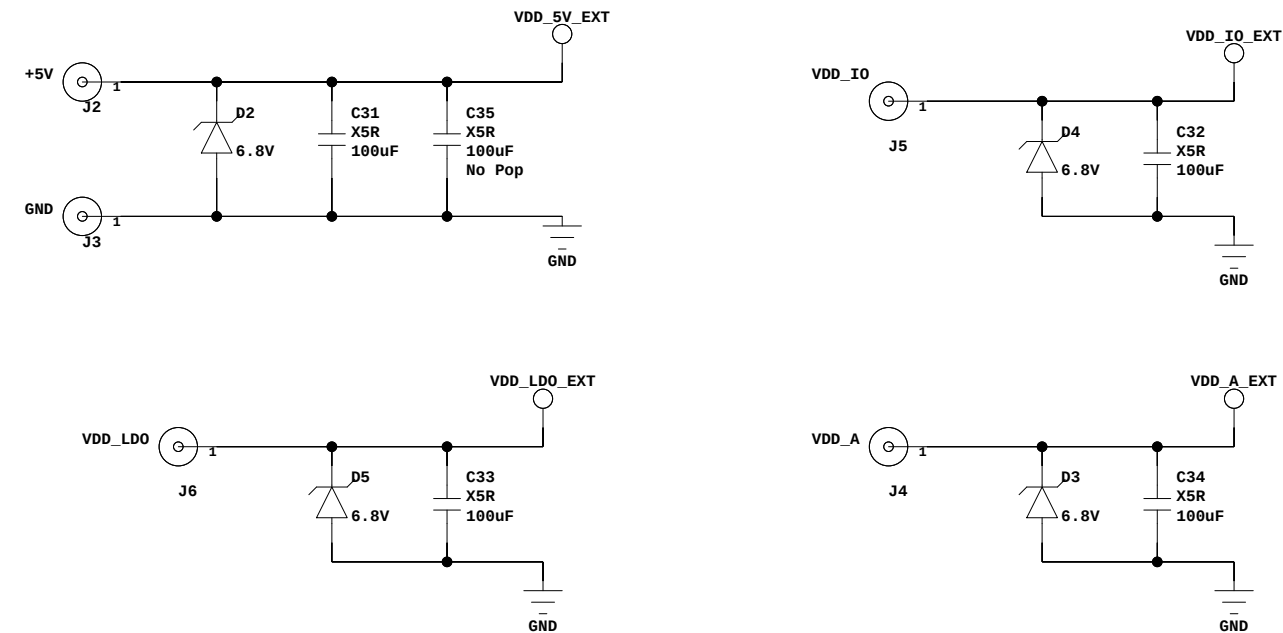
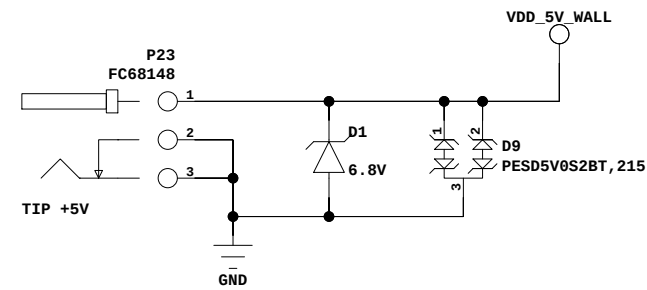


CDB82L4X-MB
"High Coast"

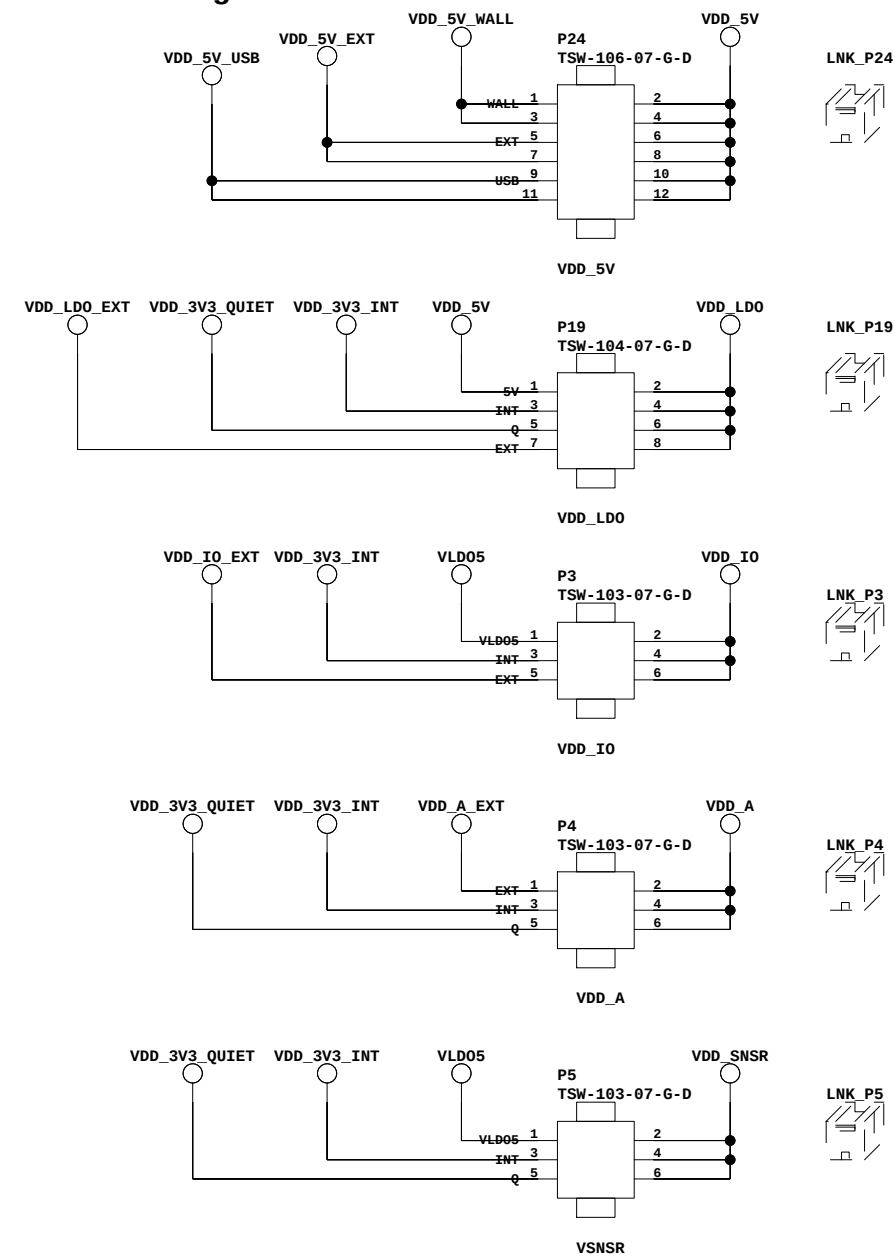
External Power



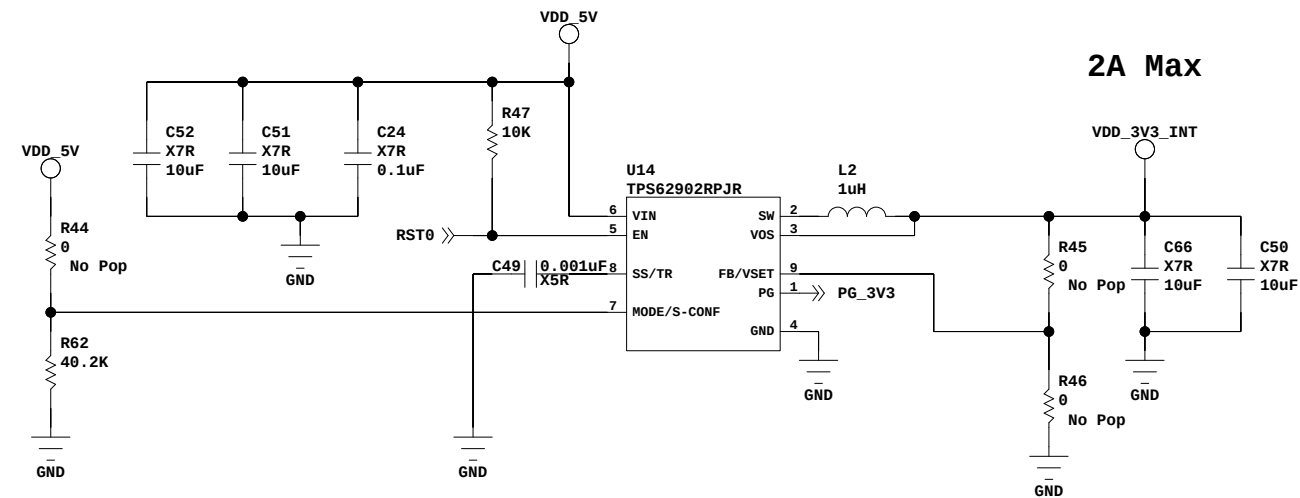
Wall Power



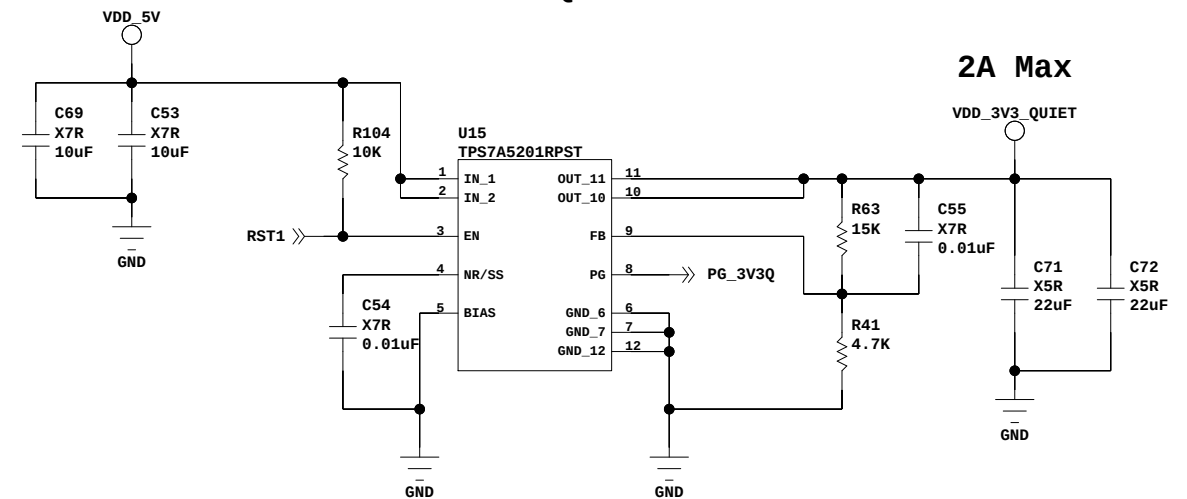
Power Muxing



VDD_3V3_INT

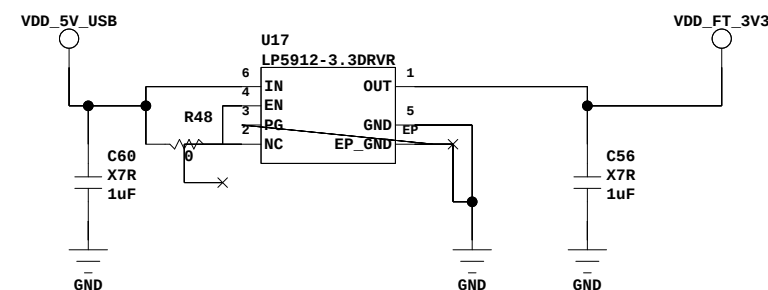


VDD_3V3_QUIET

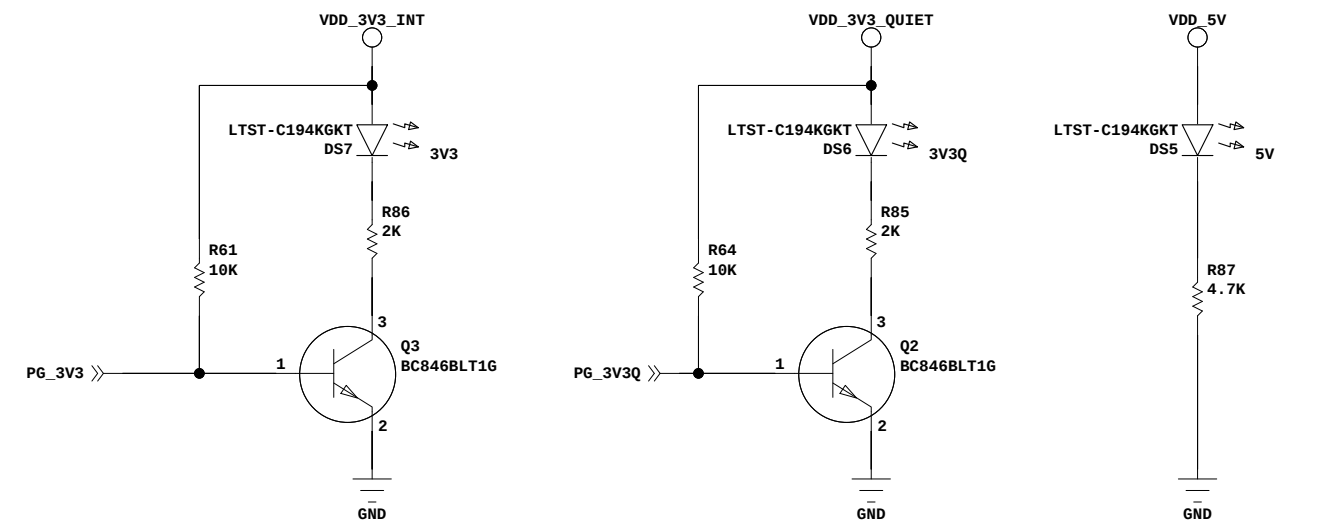


FTDI Power

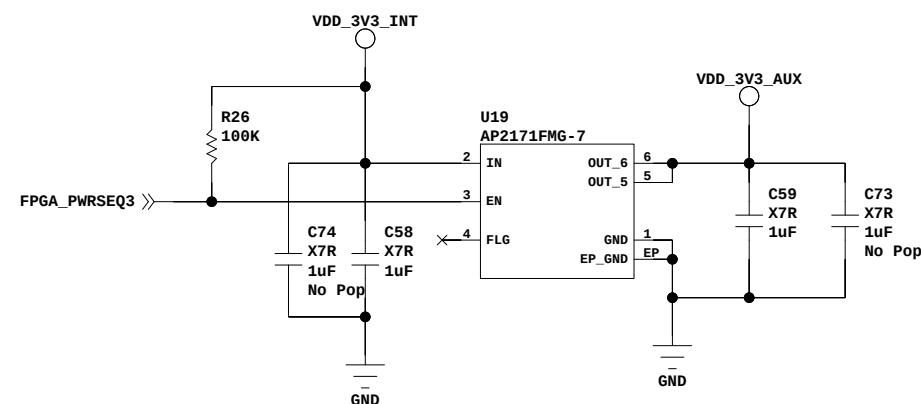
500mA max



Power OK LEDs

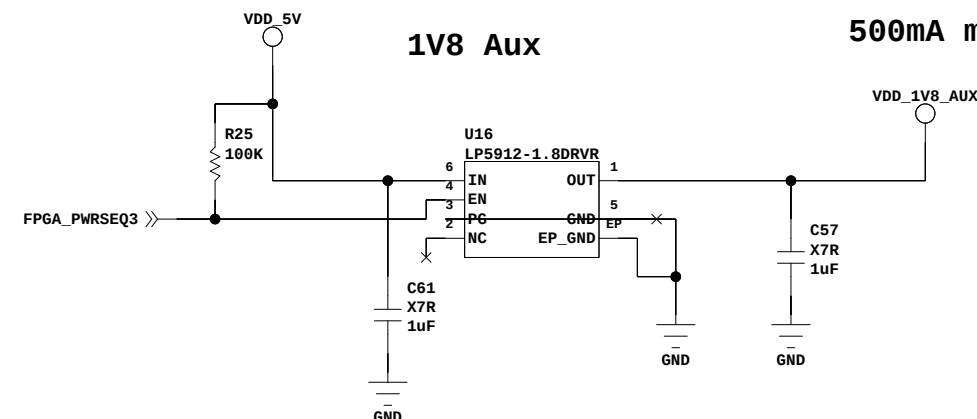


3V3 Aux Load Switch

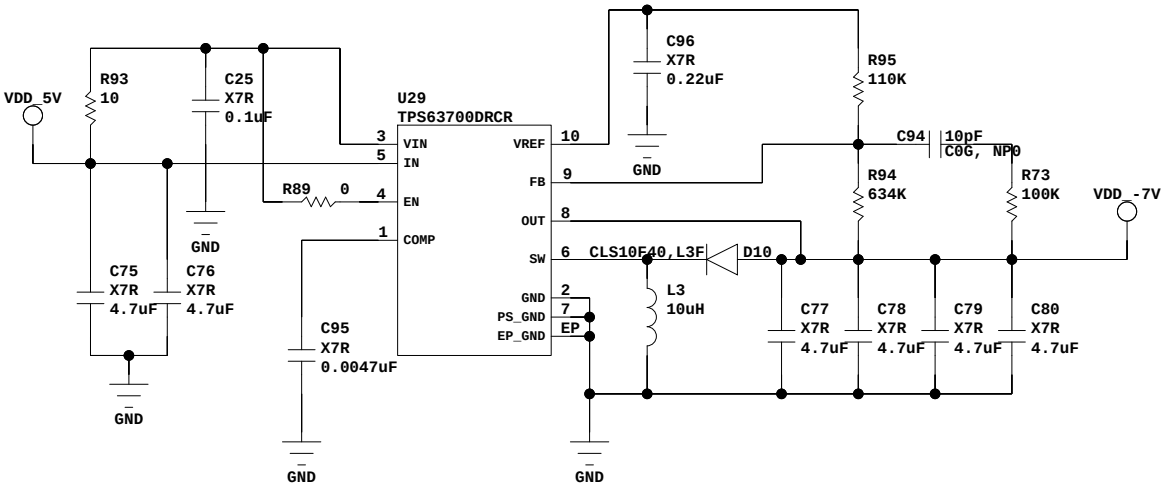


1V8 Aux

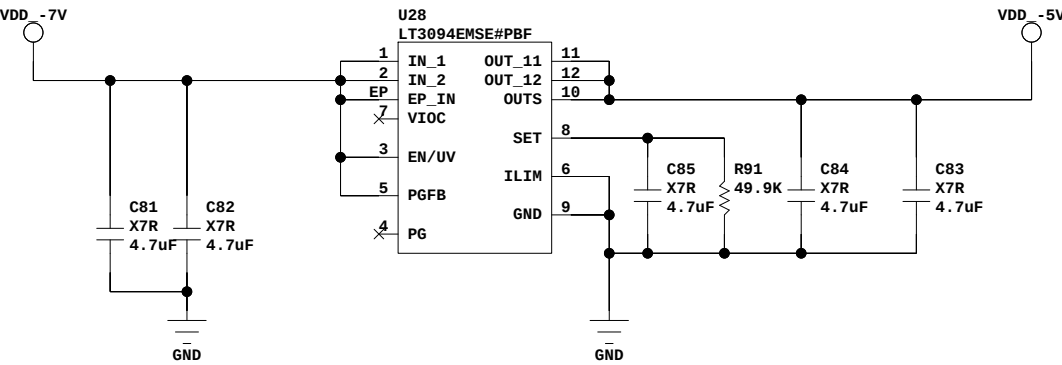
500mA max



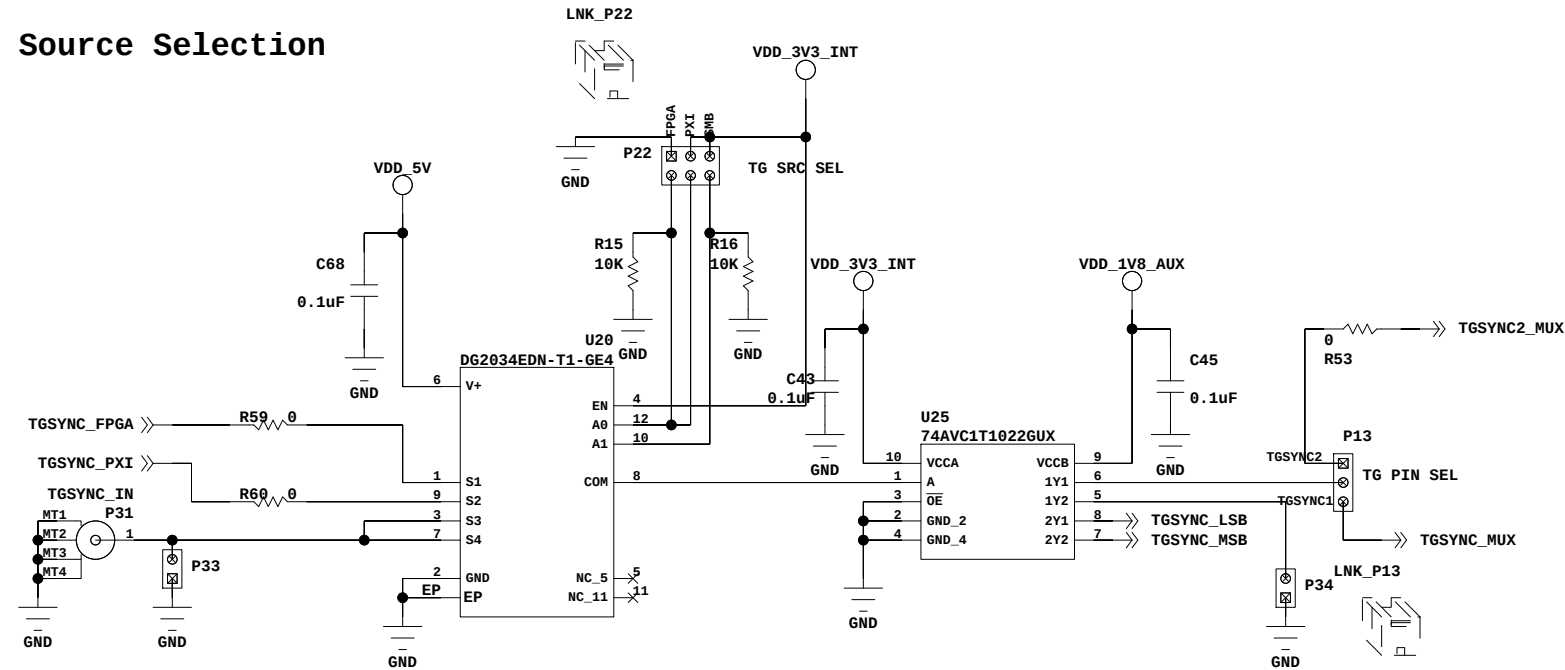
Negative 7V



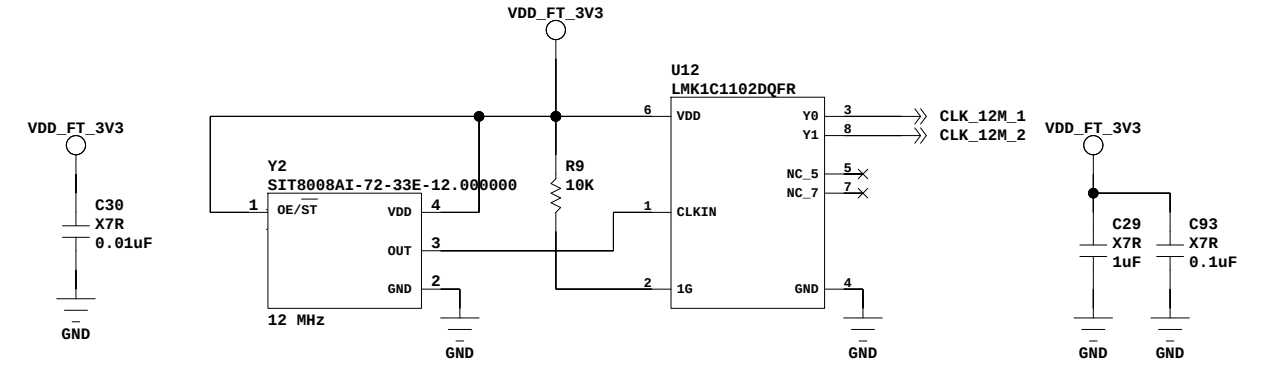
Negative 5V



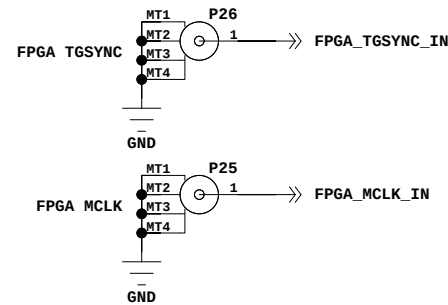
TGSYNC Source Selection



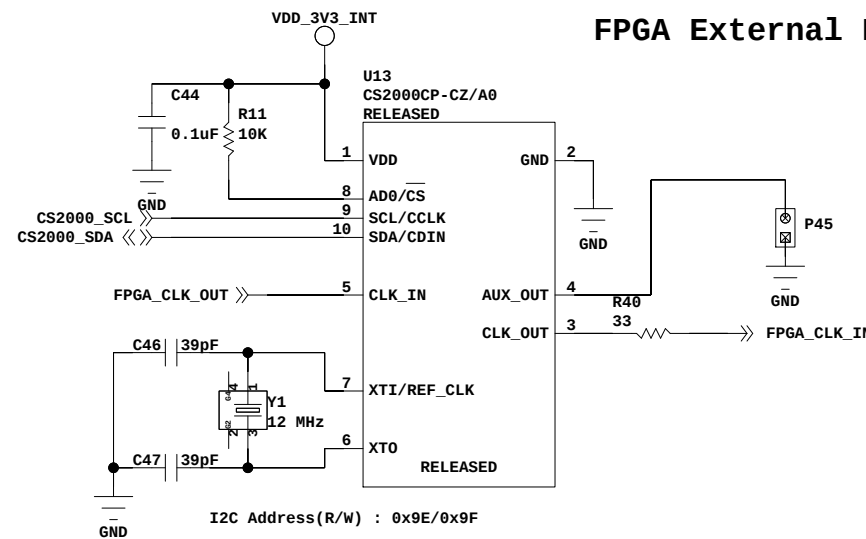
USB Clock Source



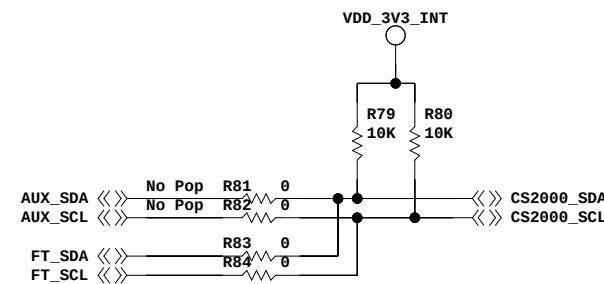
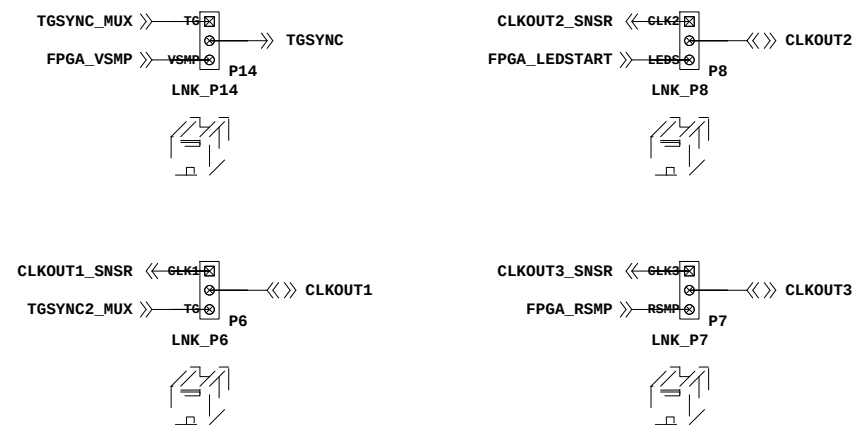
FPGA External MCLK and TGSYNC



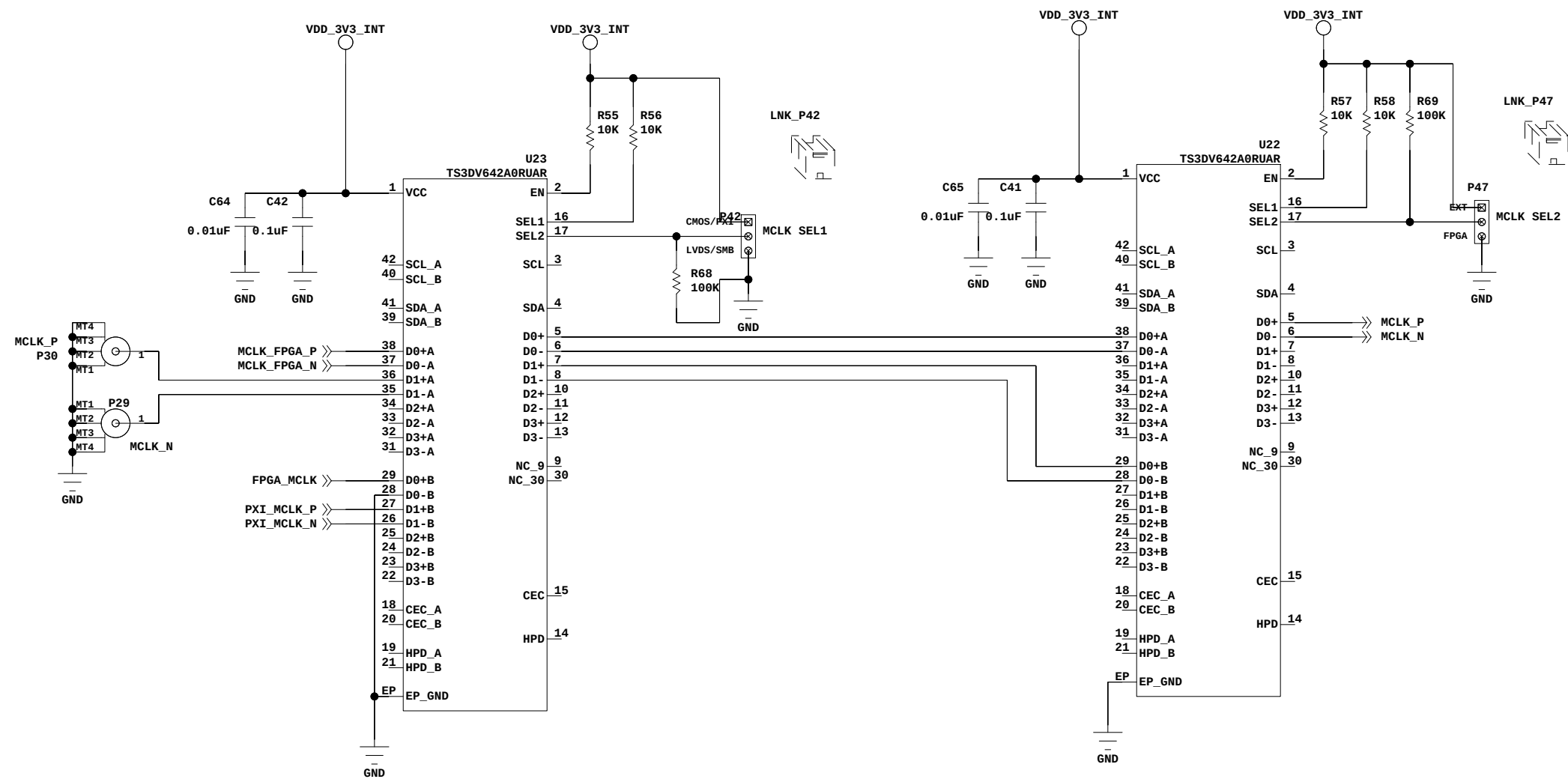
FPGA External PLL

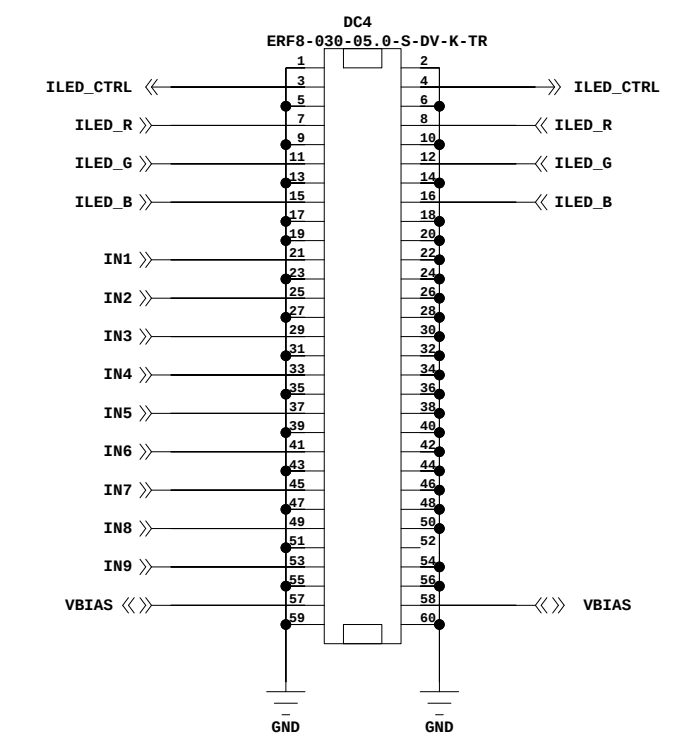
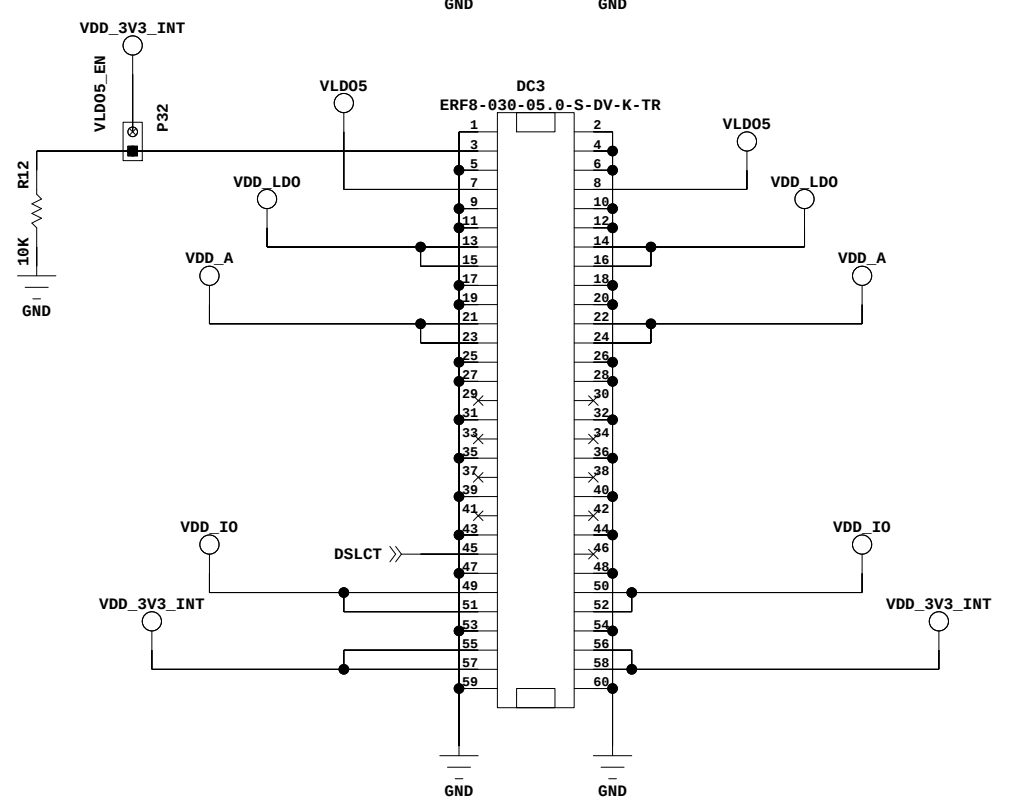
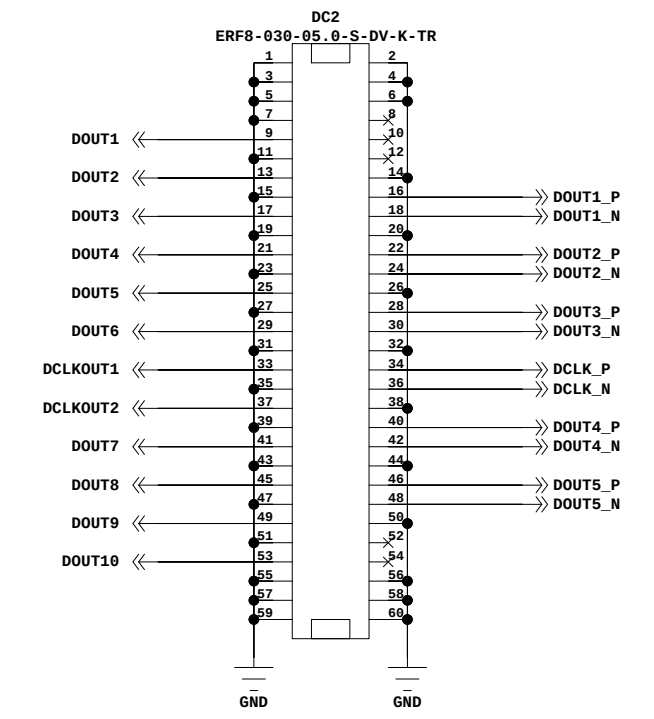
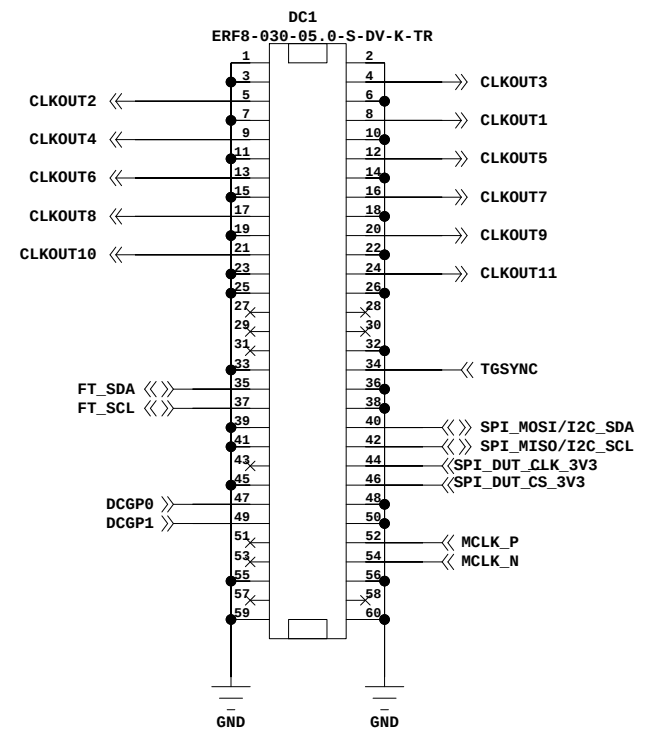


CLKOUT1-4 Mux

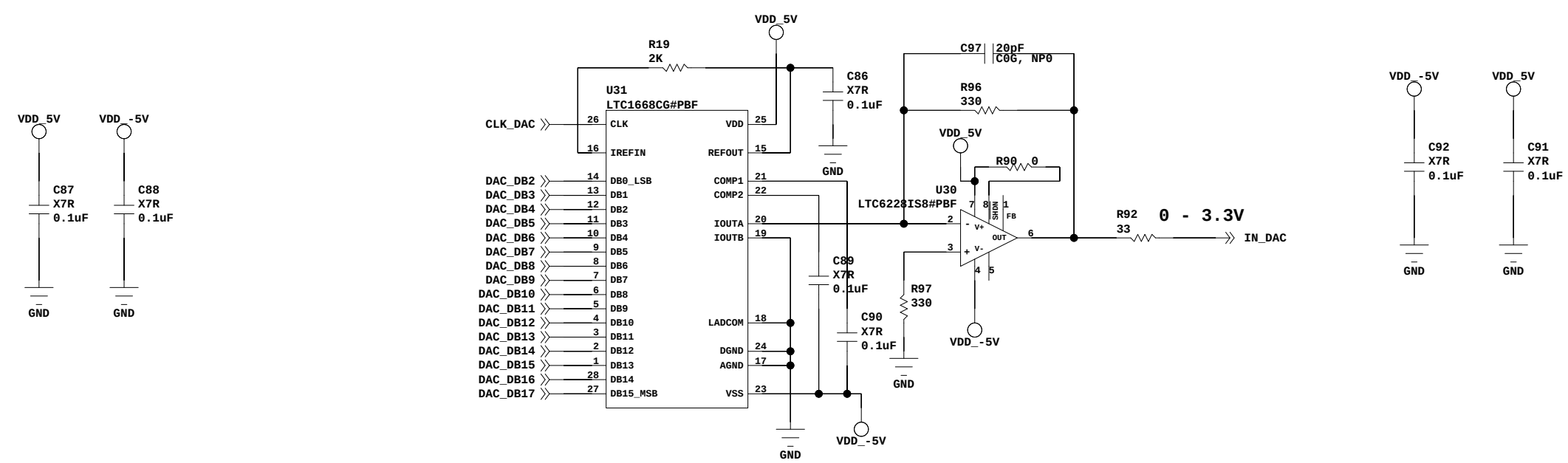


Clock Source Selection



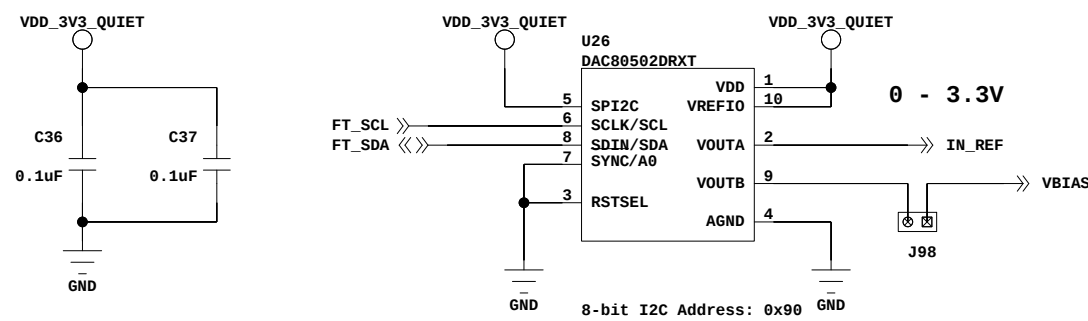


High-Speed DAC

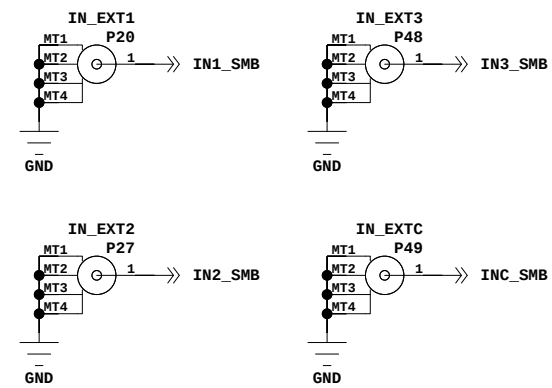


Adjustable DC Input Voltage

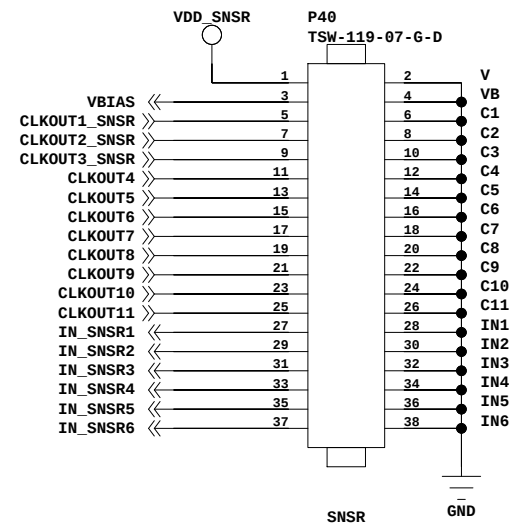
Layout: Place decoupling capacitors close to ICs



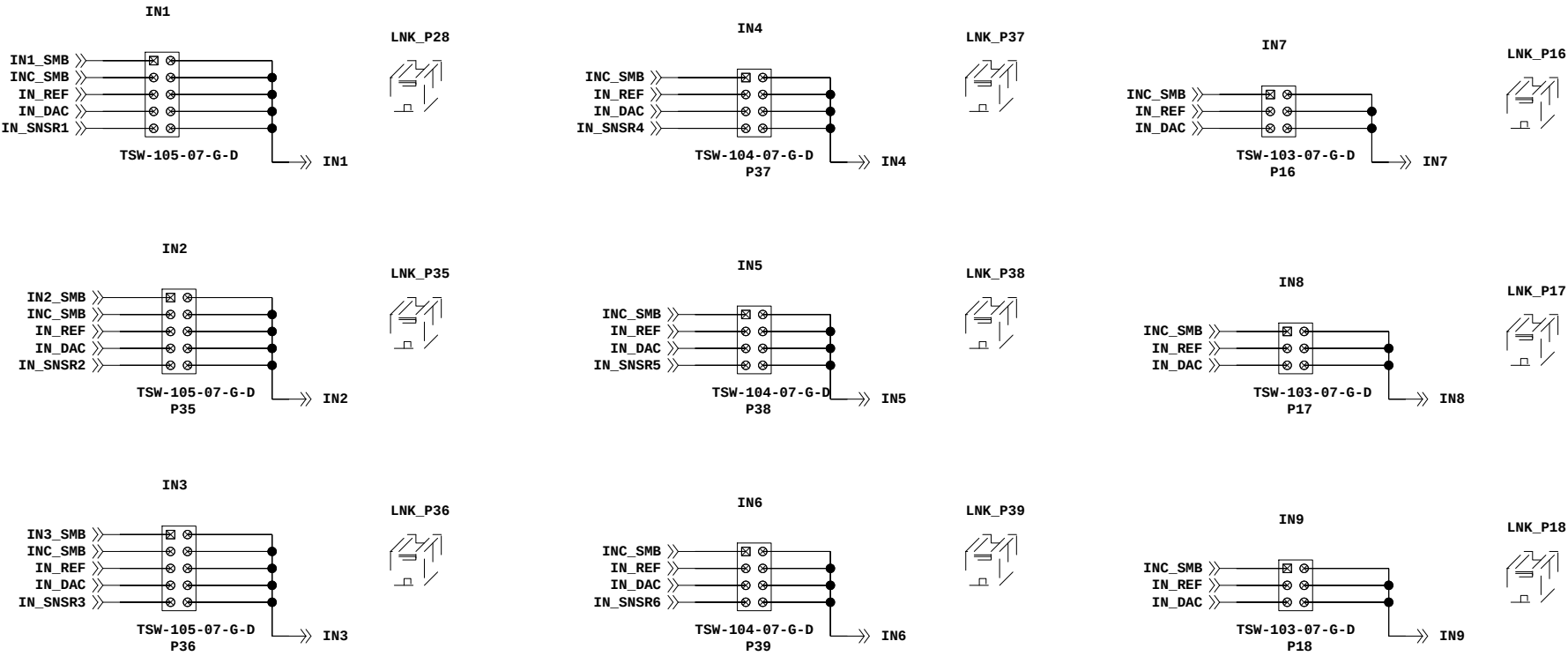
SMB Input



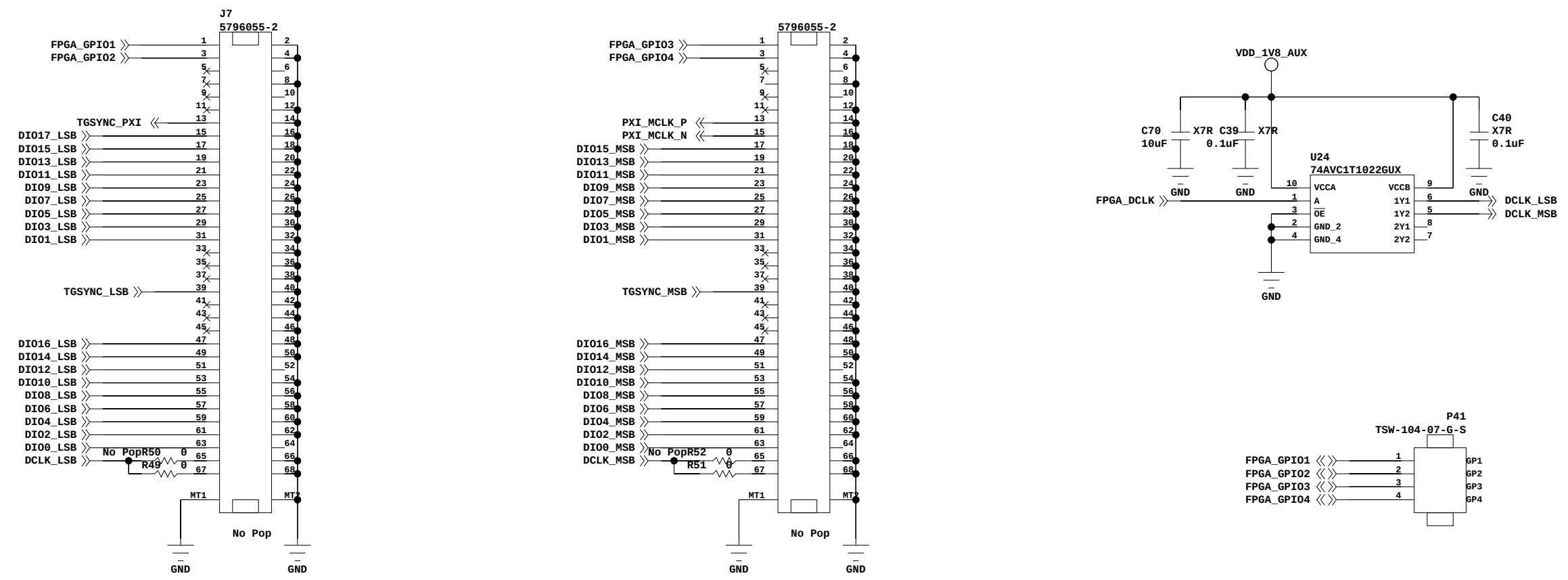
Sensor I/O Header



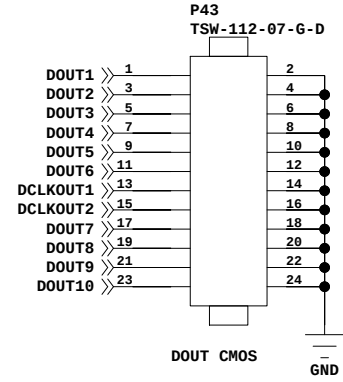
Input Source Selection



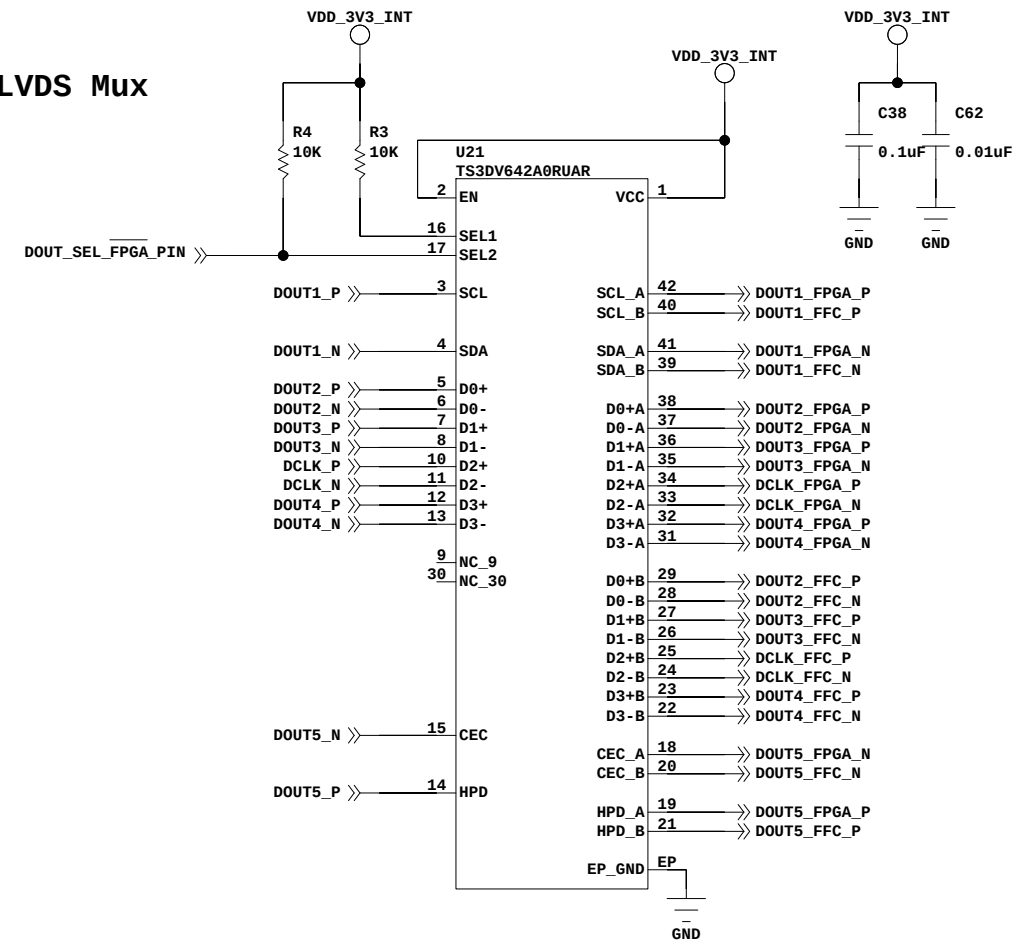
PXI Connections



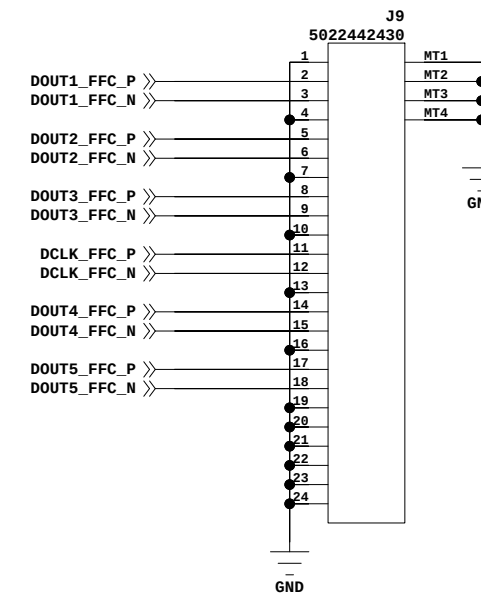
CMOS Data Out Connector

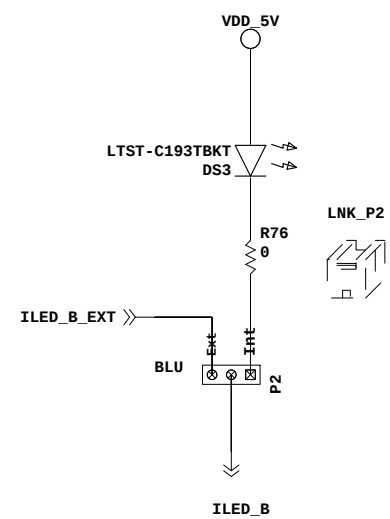
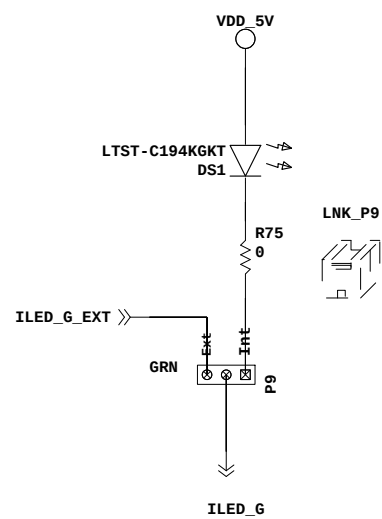
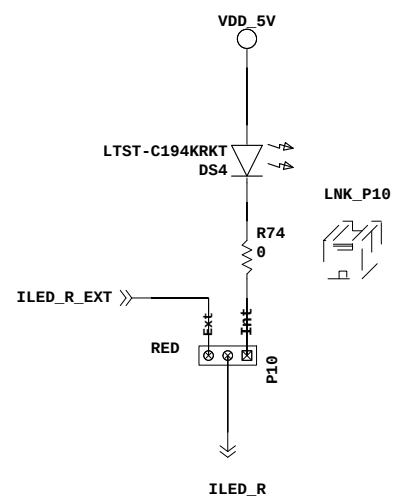
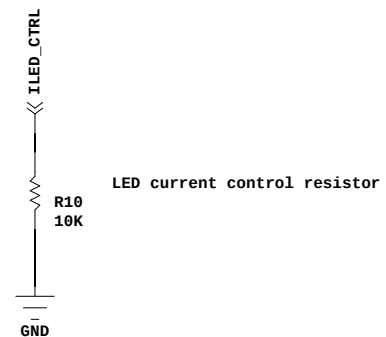
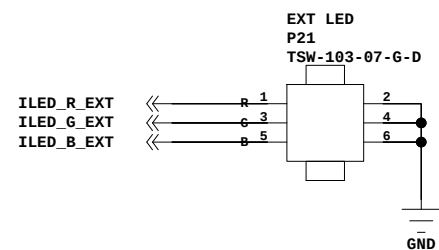


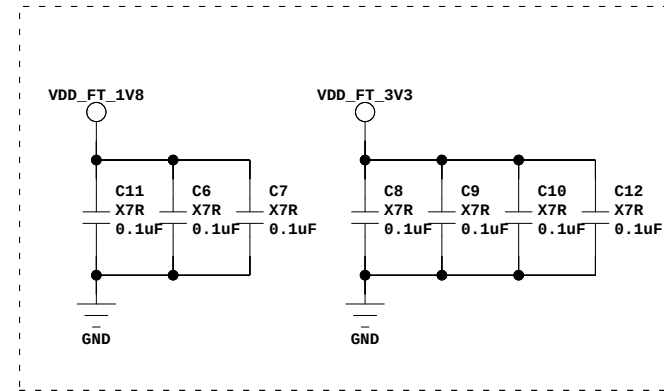
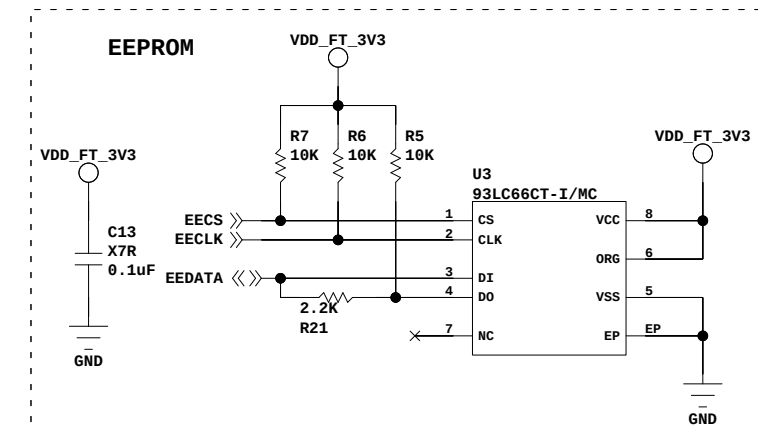
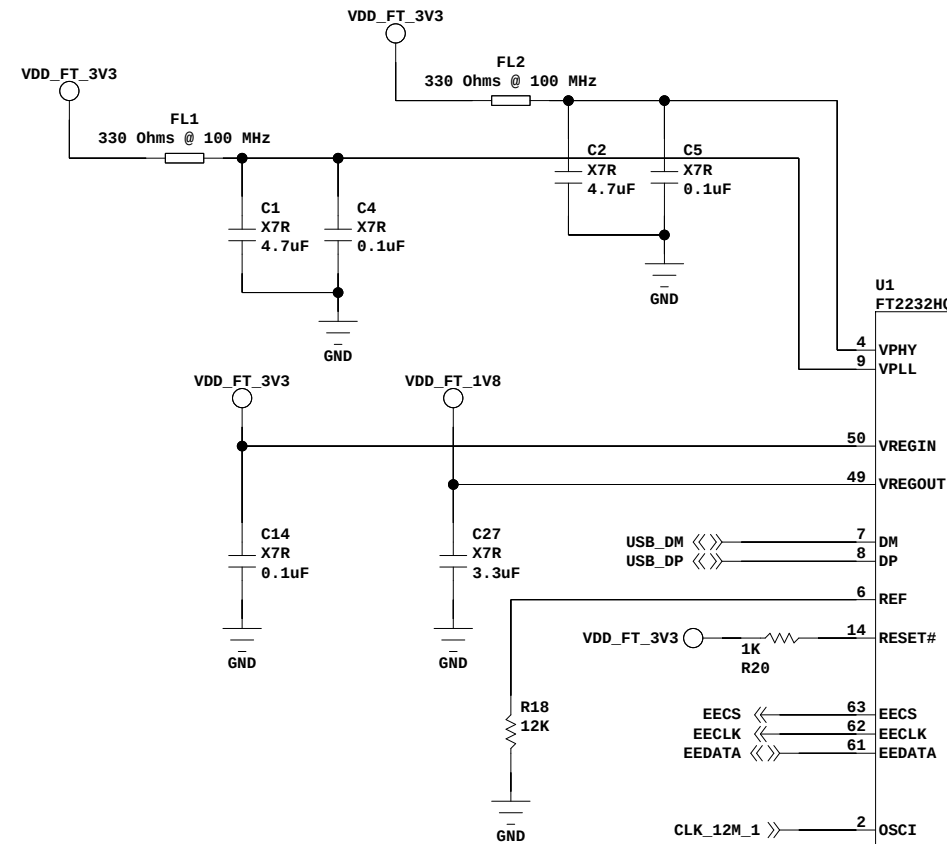
LVDS Mux



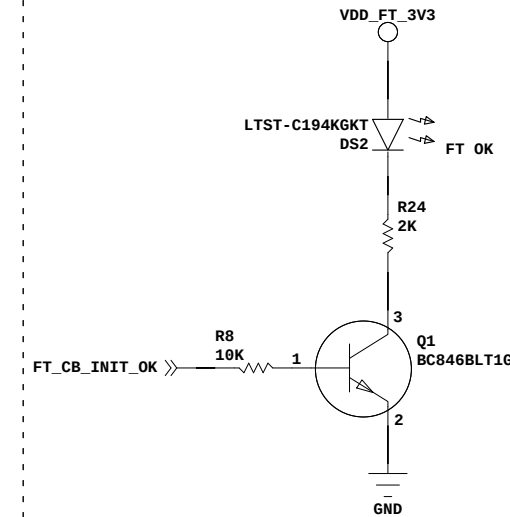
LVDS FFC Connector



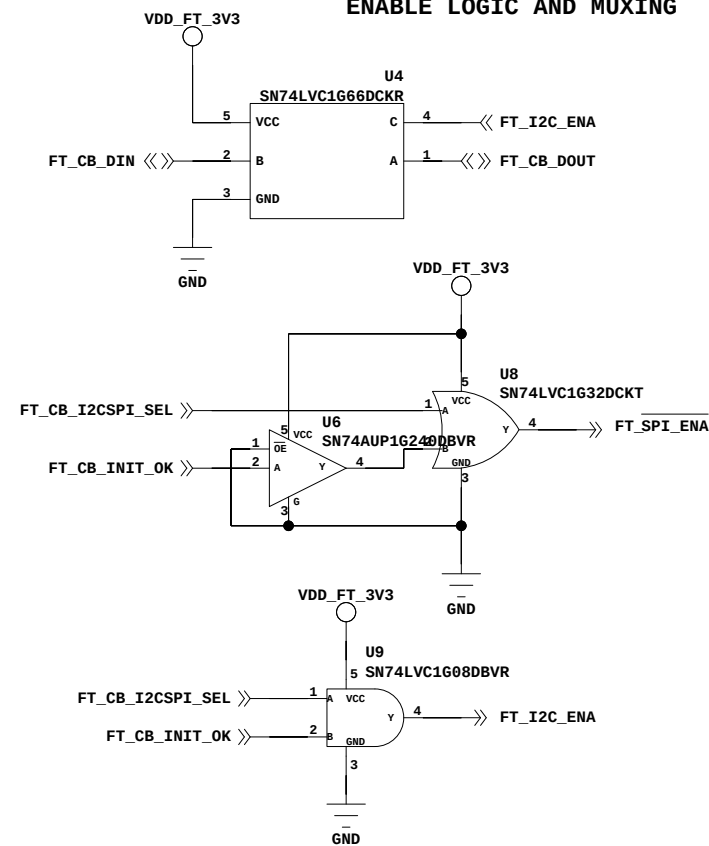




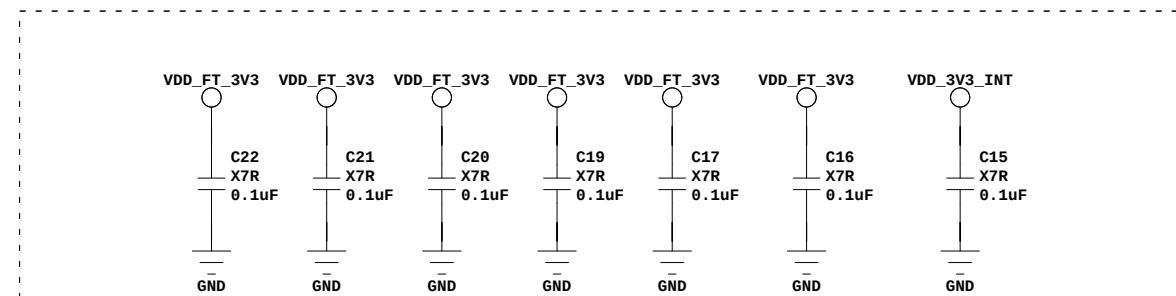
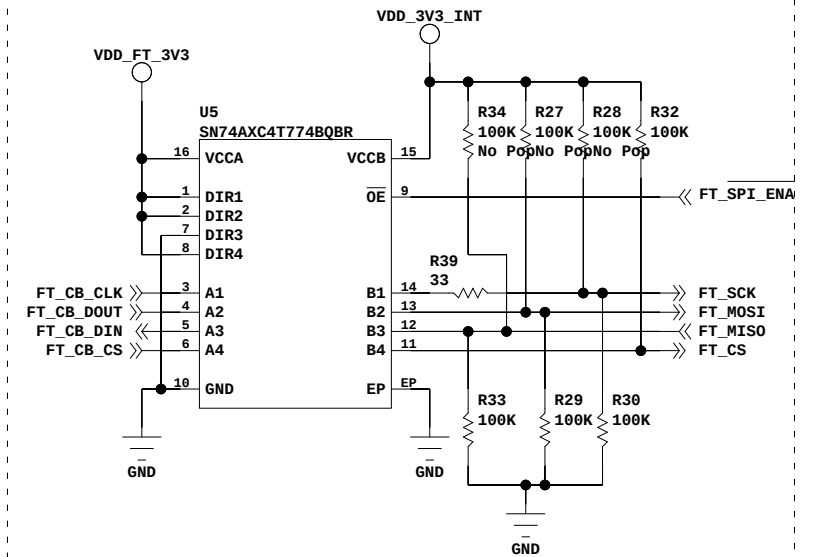
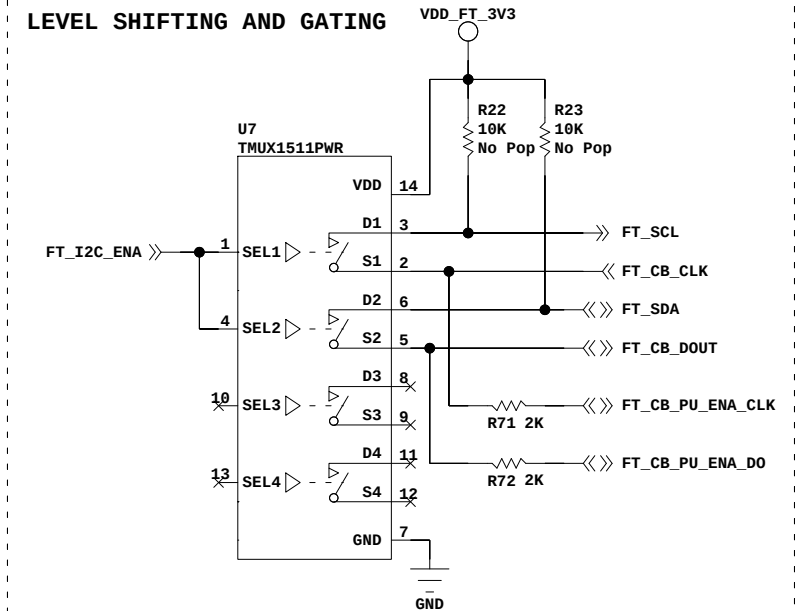
INIT LED



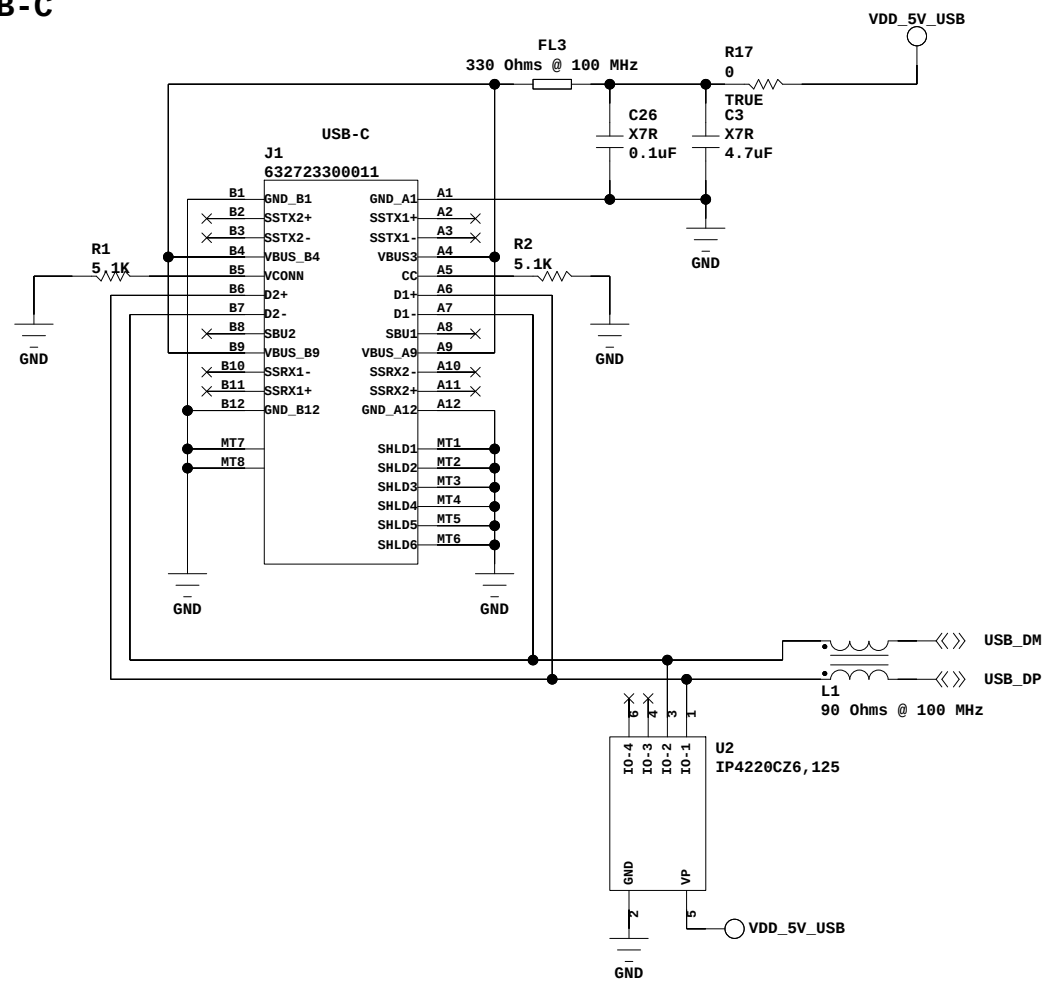
ENABLE LOGIC AND MUXING



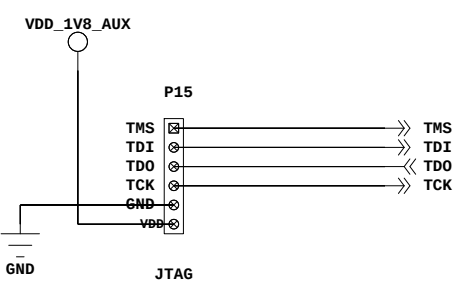
LEVEL SHIFTING AND GATING



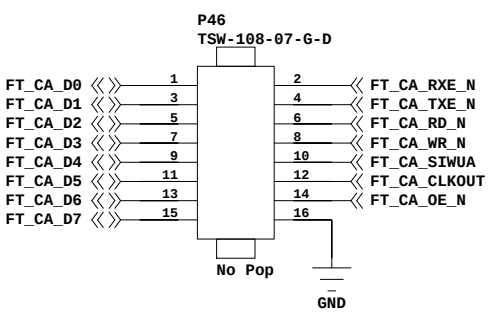
USB-C



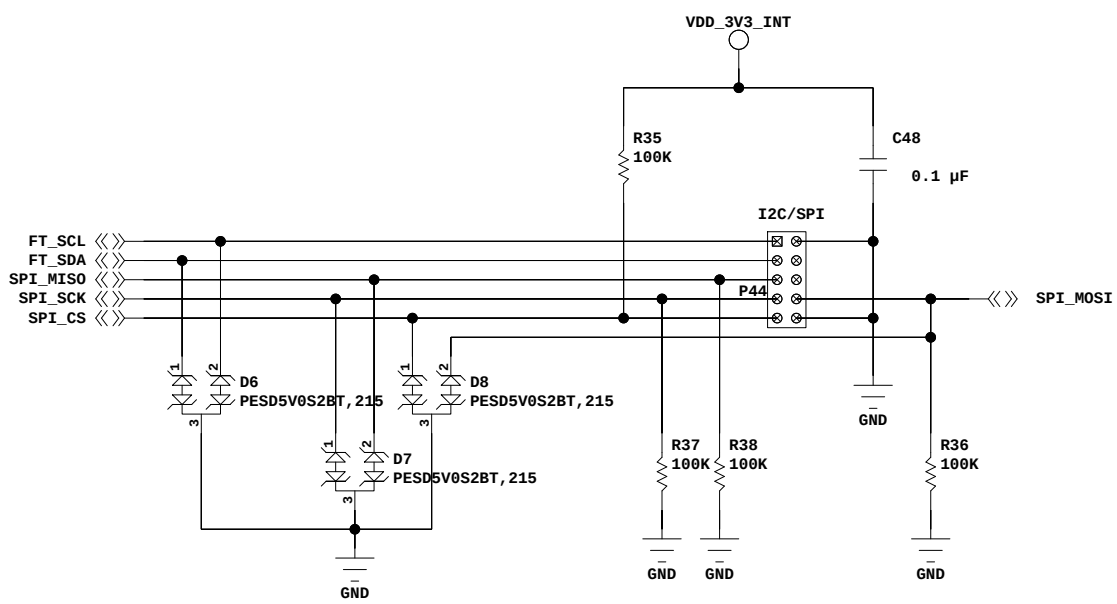
JTAG



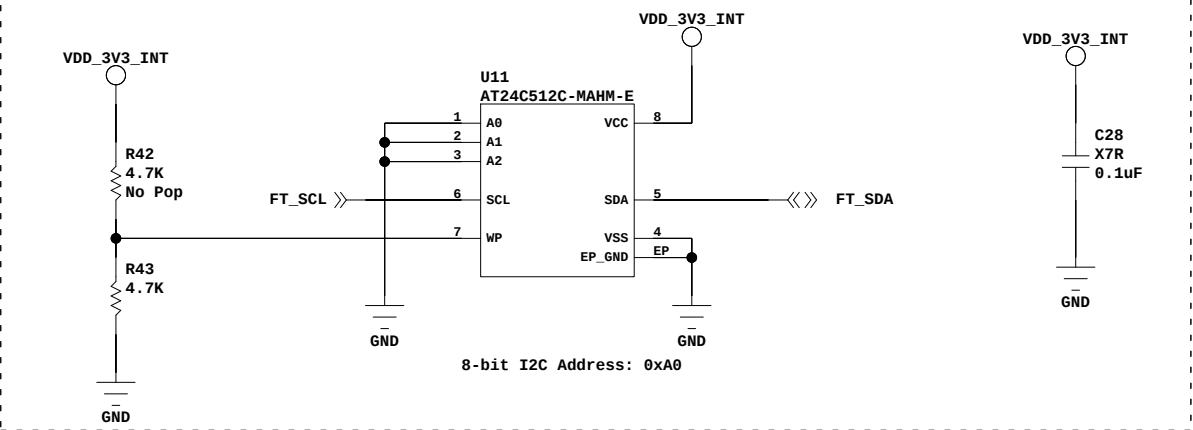
Sync Fifo Test Points



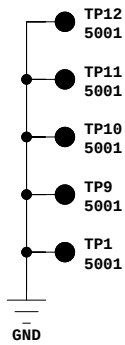
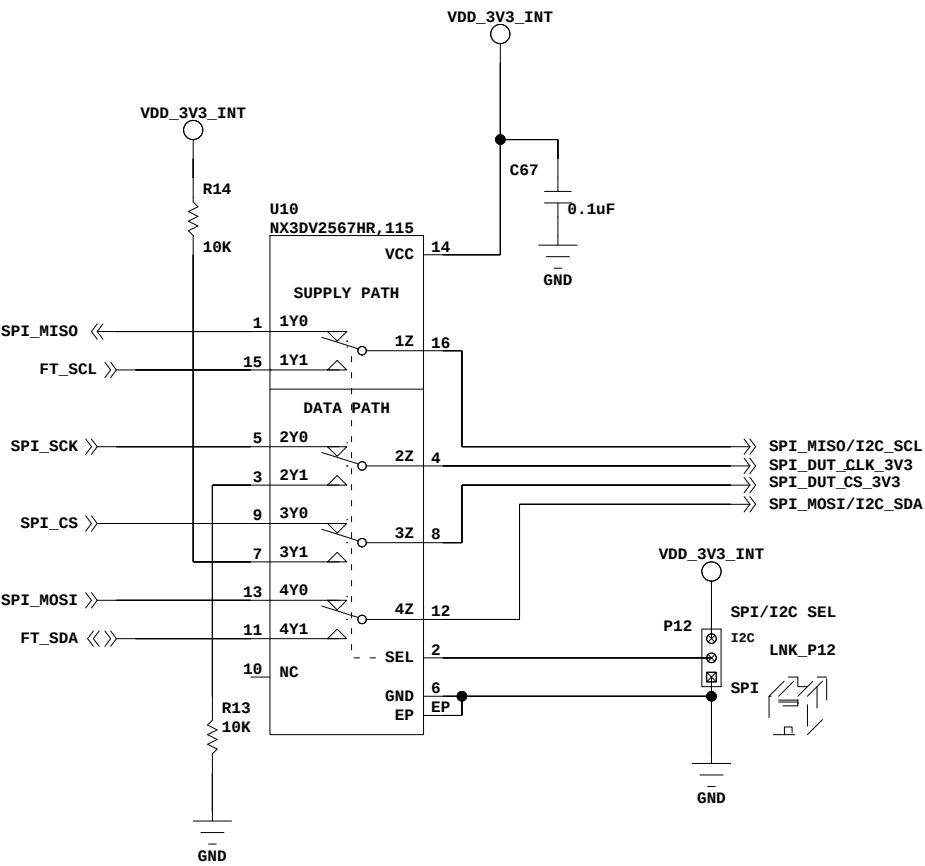
External I2C/SPI



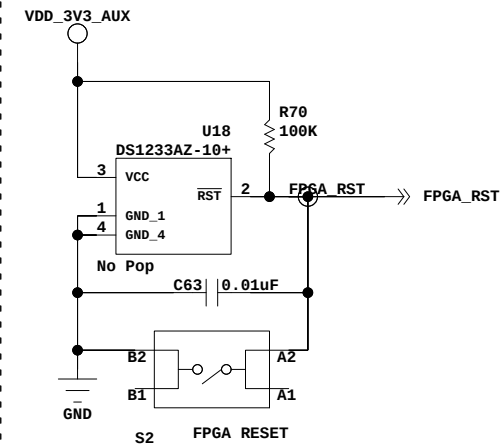
Board ID EEPROM



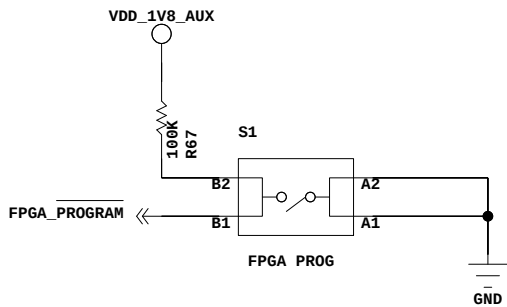
I2C/SPI Mux



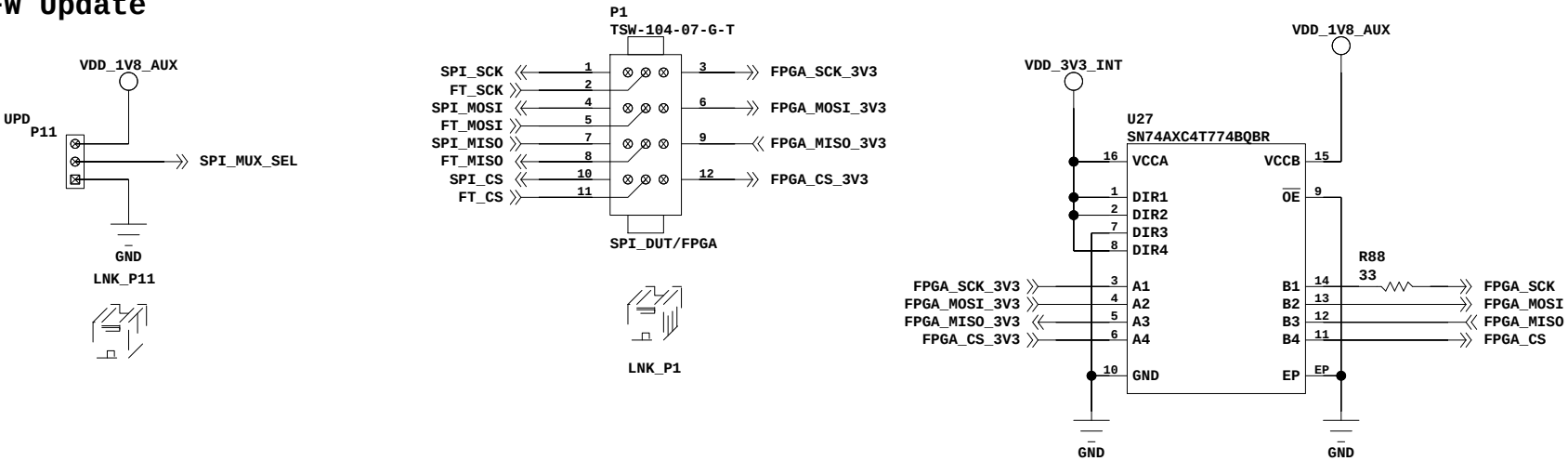
FPGA Reset



FPGA Prog



FPGA FW Update



FPGA Module Standoffs



Hex Standoffs

