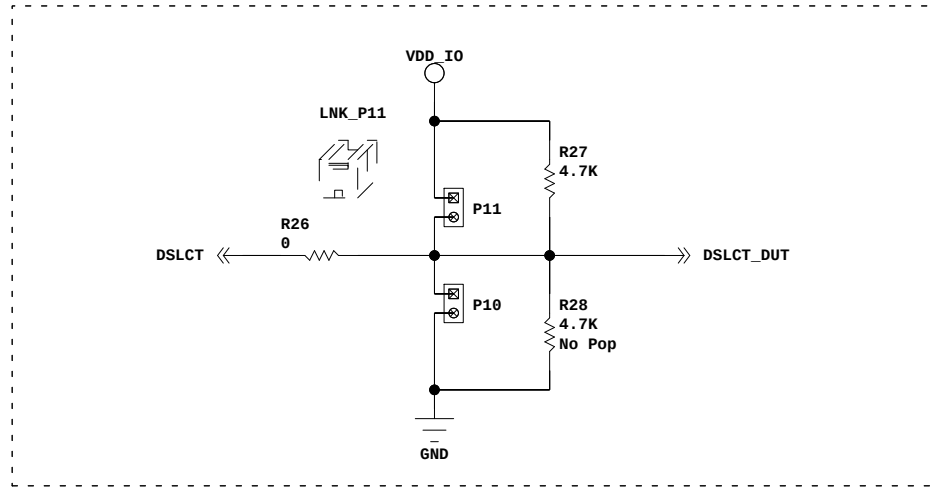
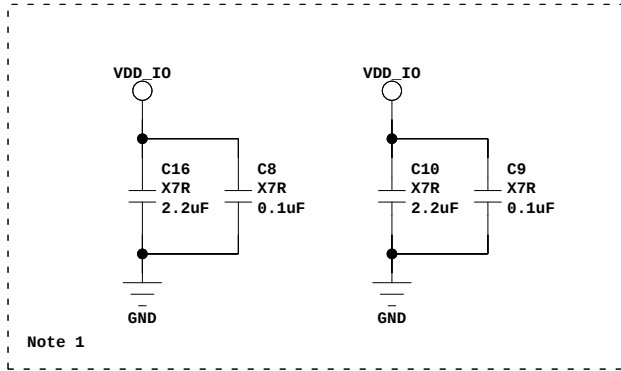
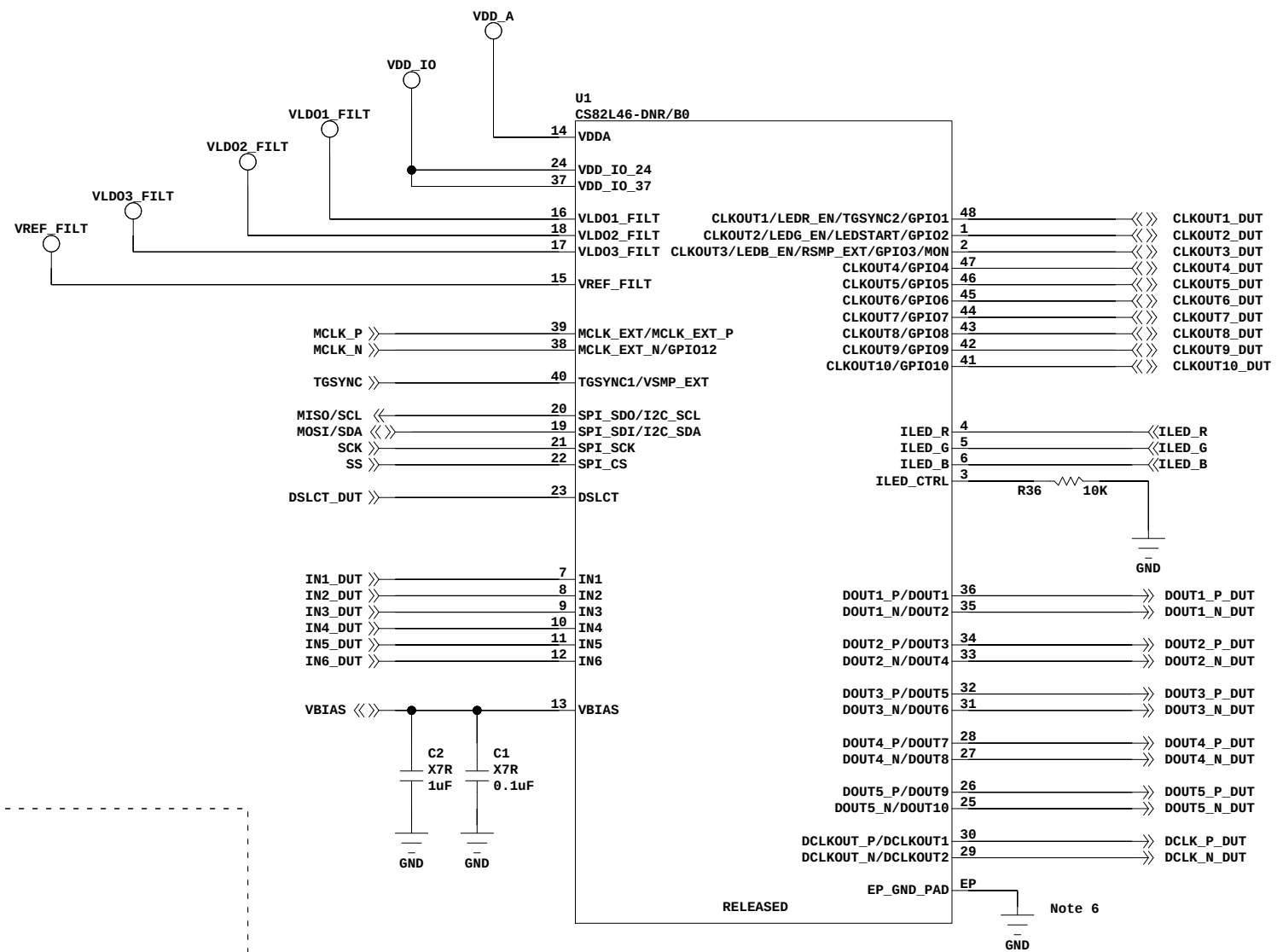
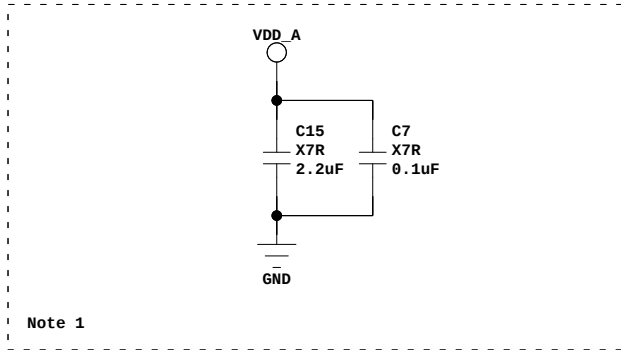
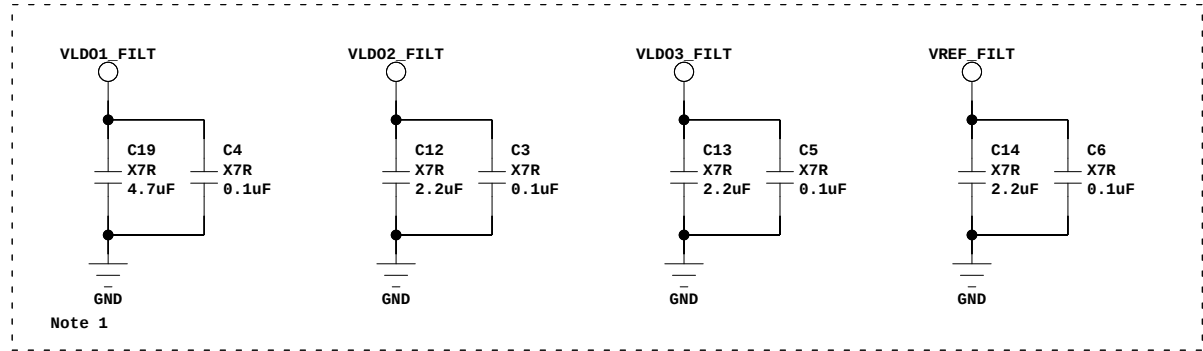


CDB82L46 - DC

REVISION HISTORY:

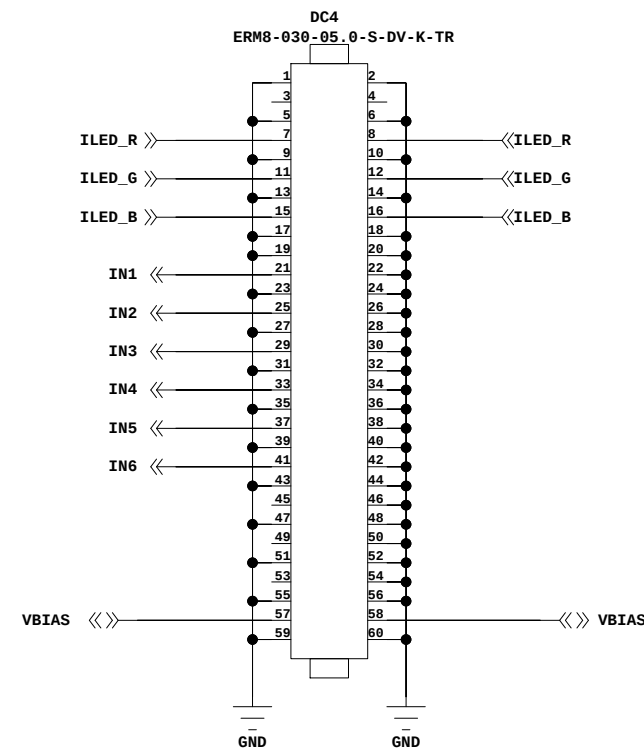
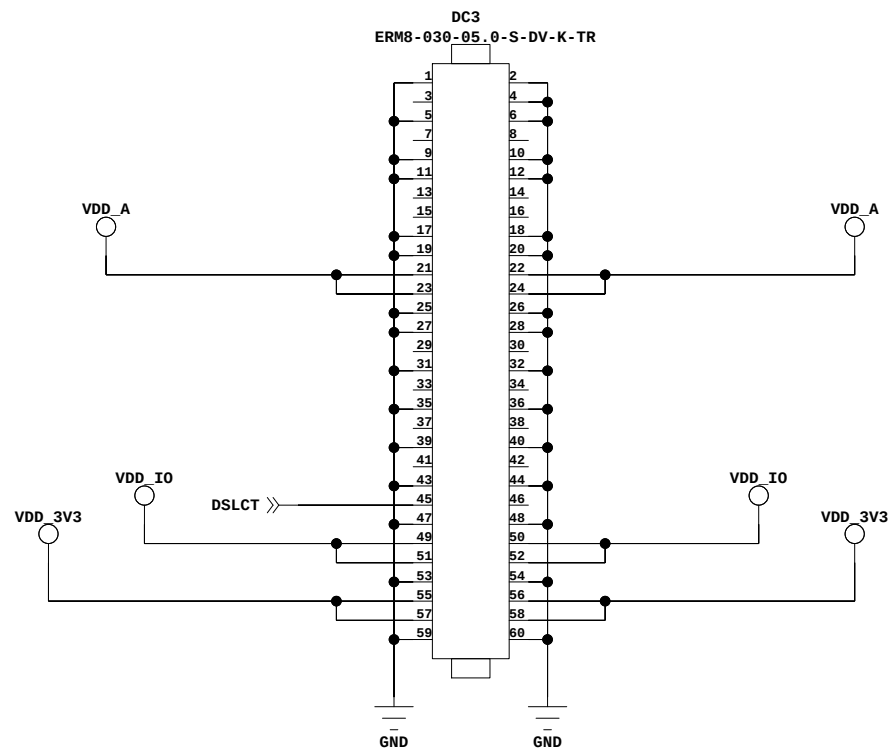
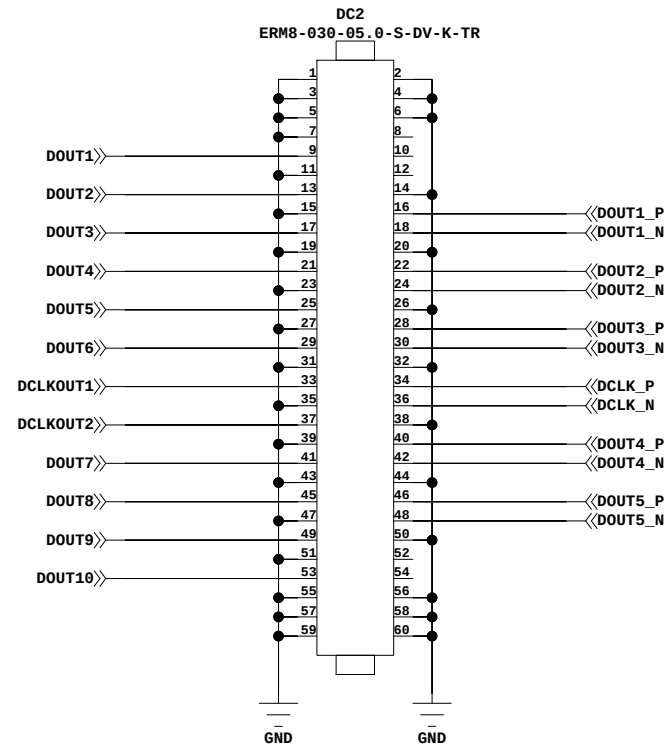
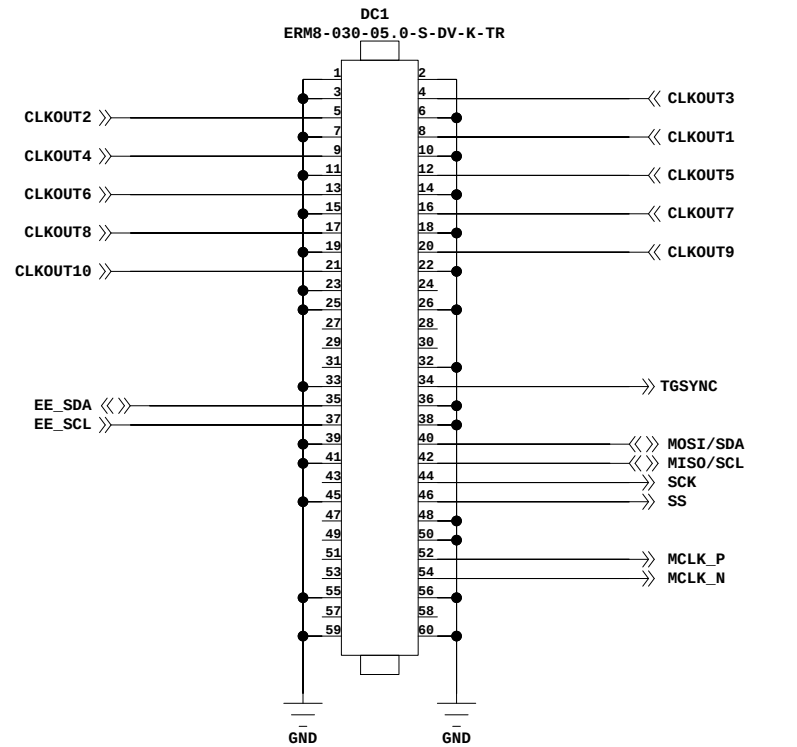
- A0 - Initial Revision
- B0 - Silkscreen updates no schematic changes.
- B1 - Updating device symbol to Rev B, no PCB change





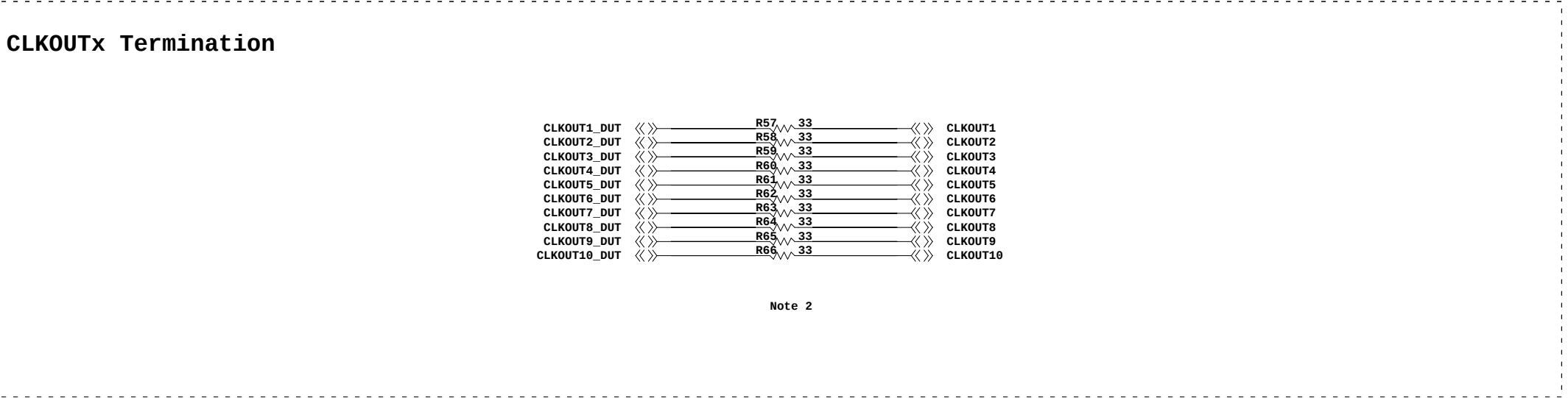
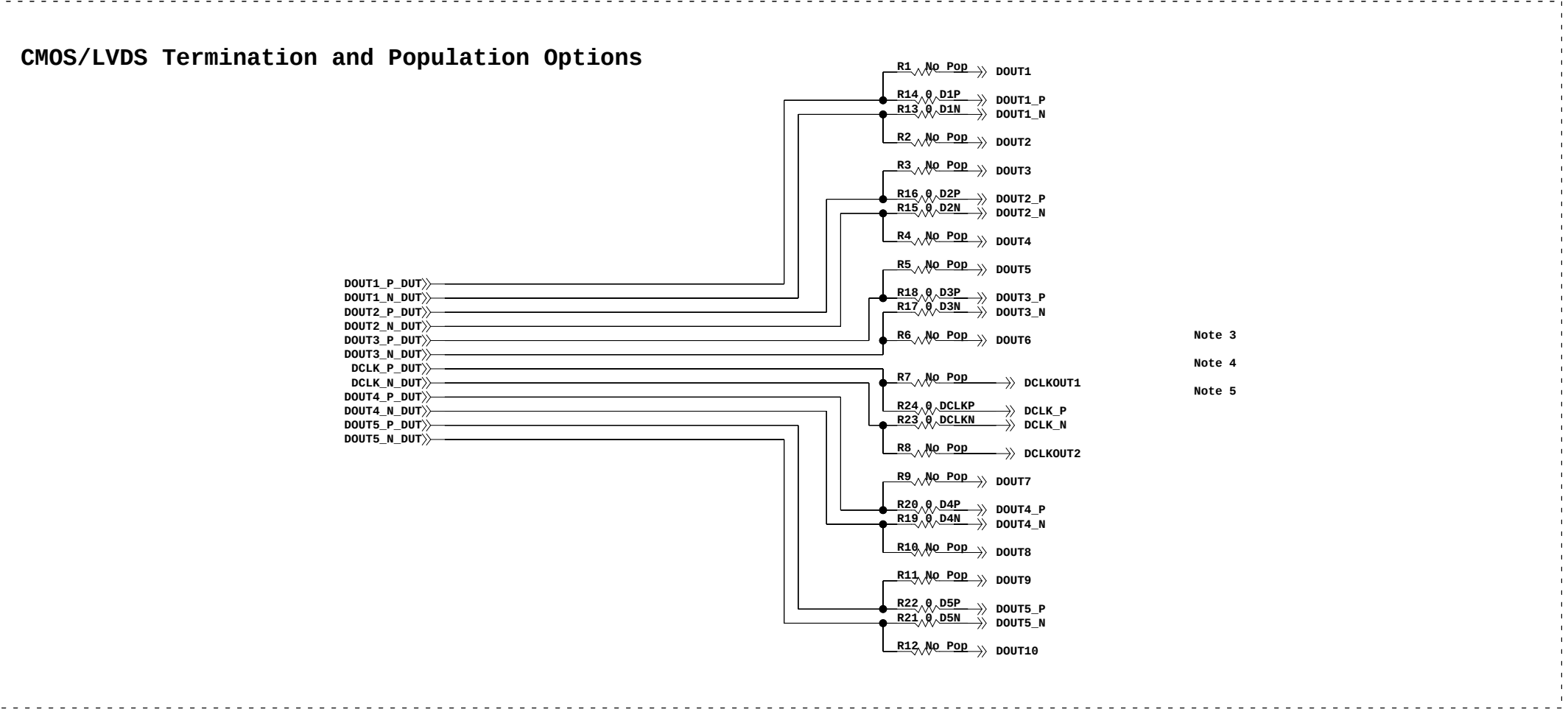
Layout Notes
See Layout Document
CDB82L46 Schematic and Layout Guidelines





Layout Notes
See Layout Document
CDB82L46 Schematic and Layout Guidelines

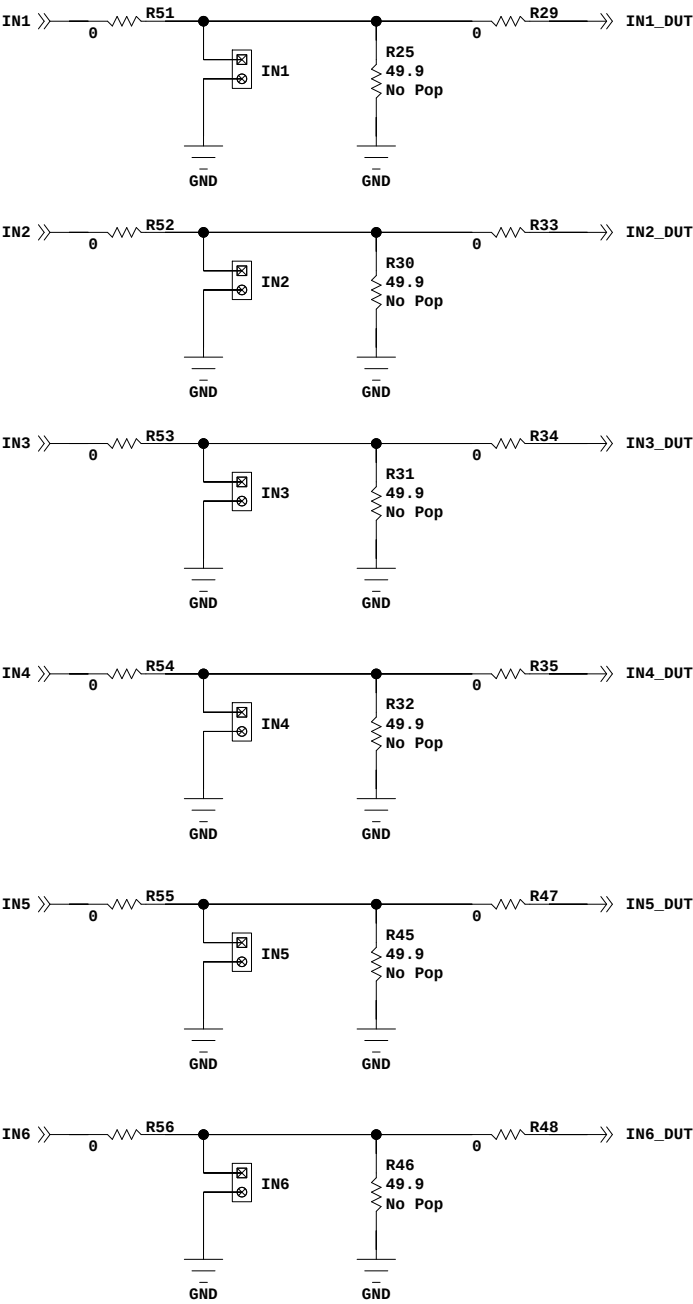




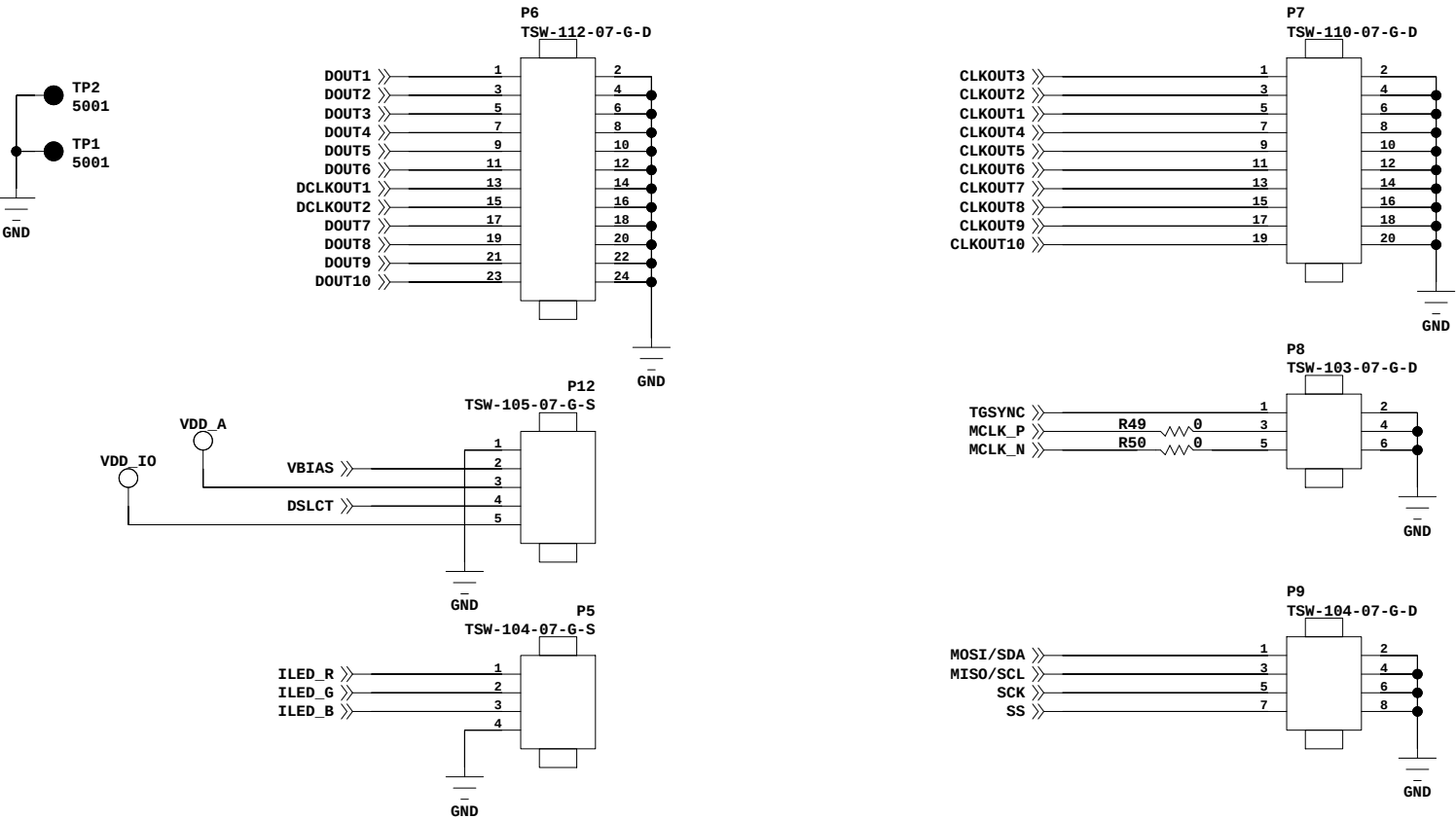
Layout Notes
See Layout Document
CDB82L46 Schematic and Layout Guidelines



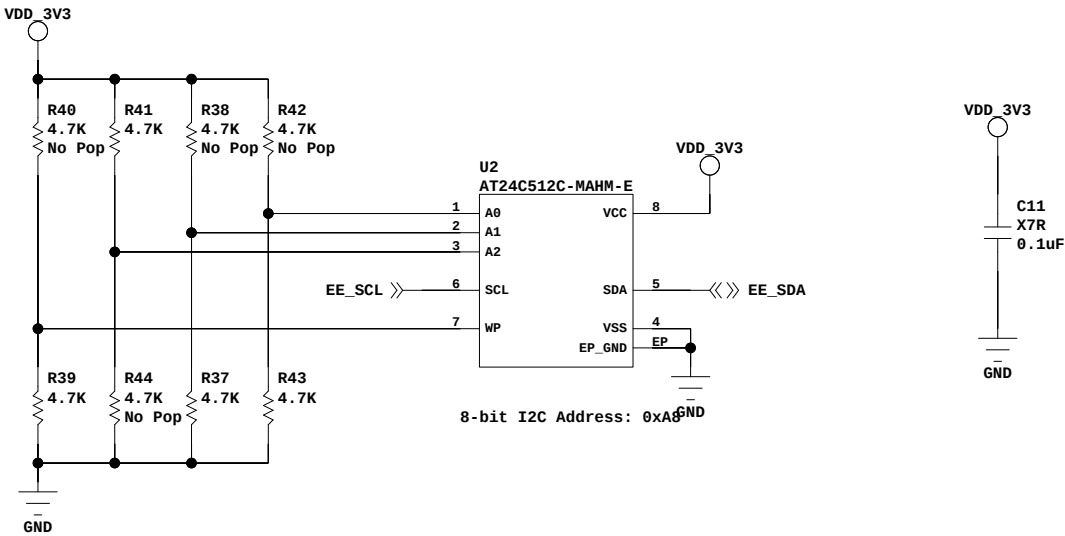
Input



Test Points



Board ID EEPROM



Layout Notes

See Layout Document
CDB82L46 Schematic and Layout Guidelines

