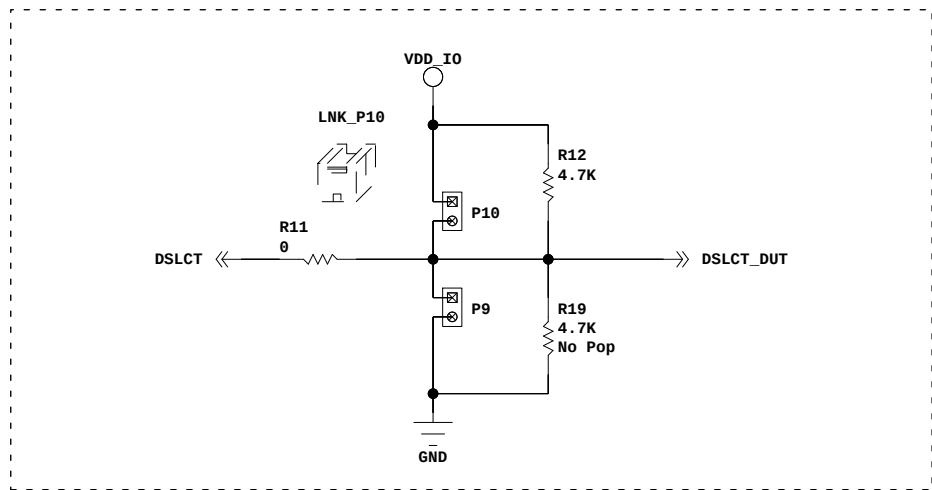
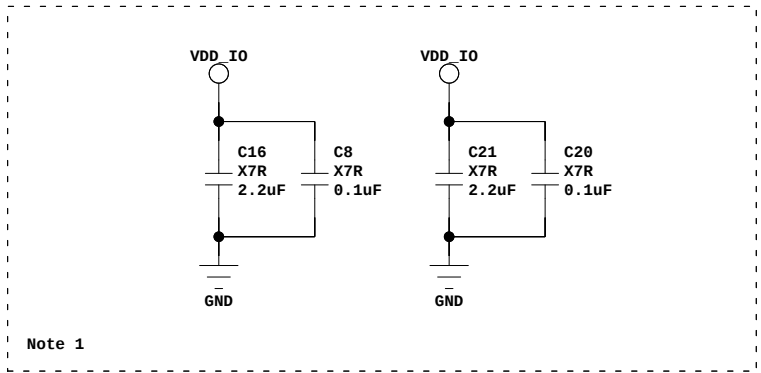
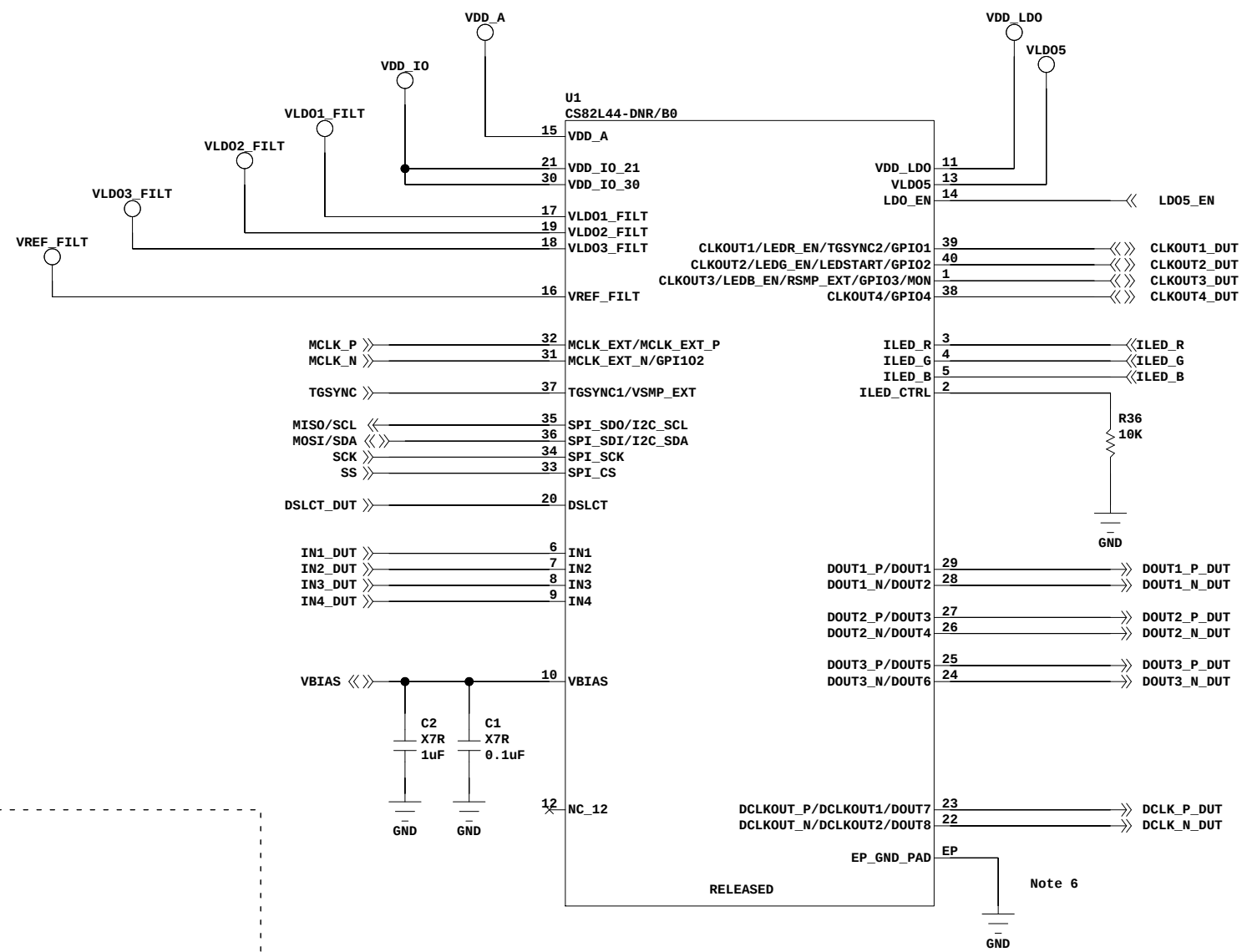
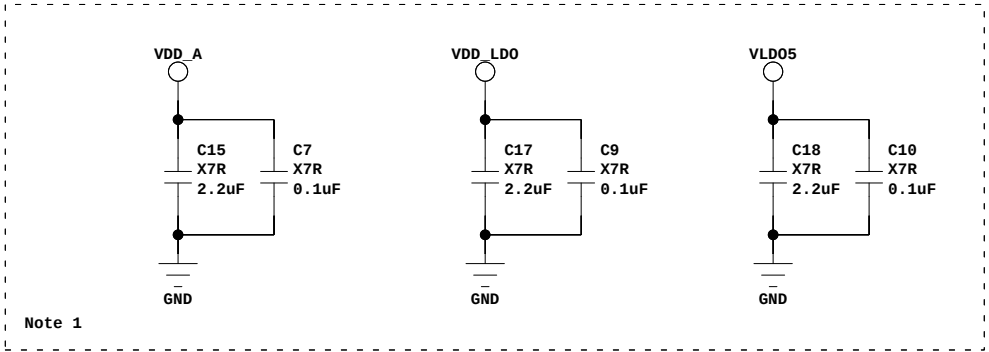
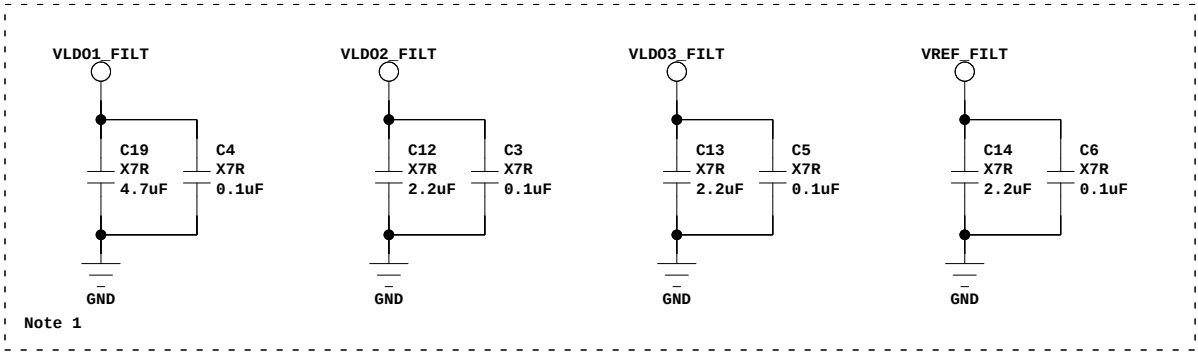


CDB82L44 - DC

REVISION HISTORY:

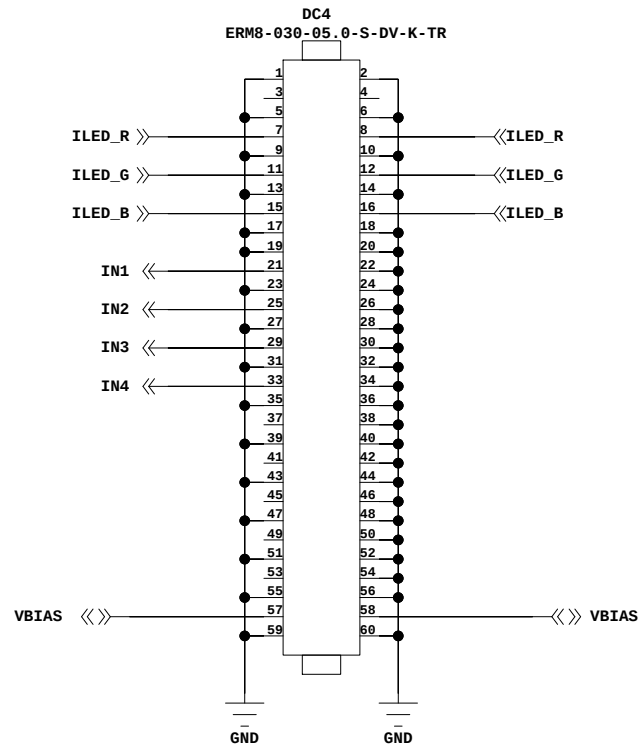
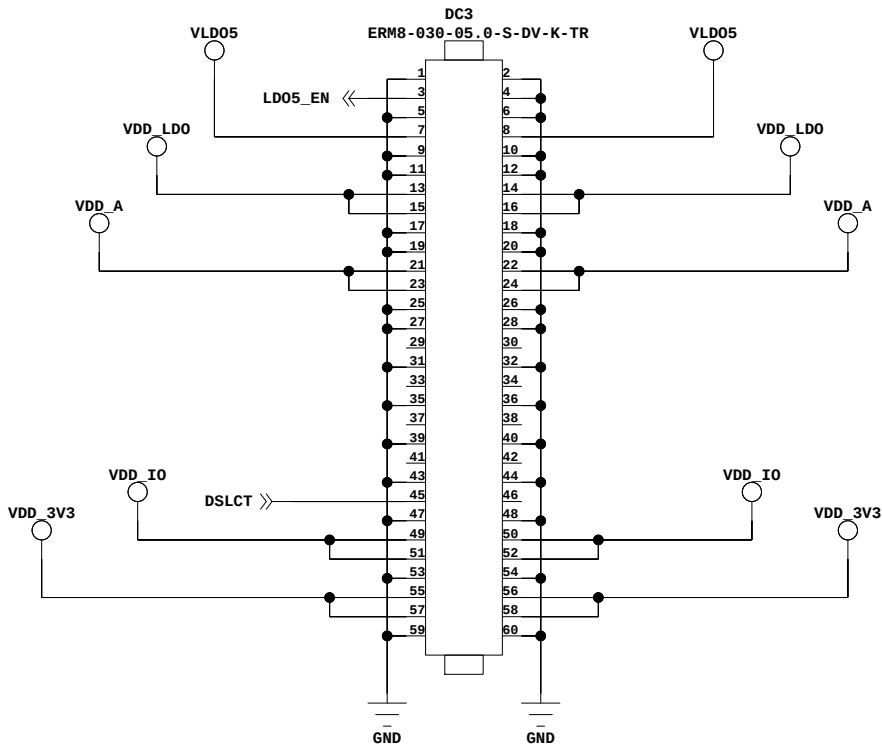
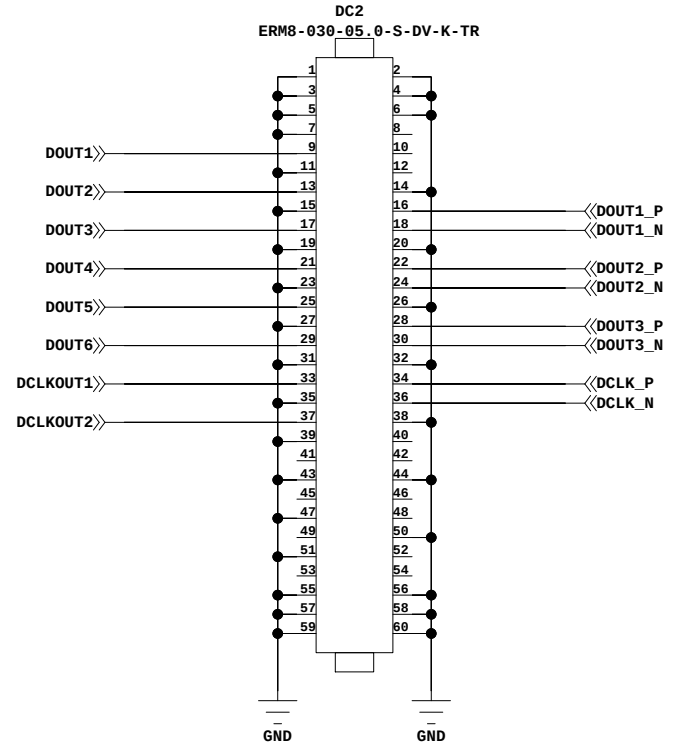
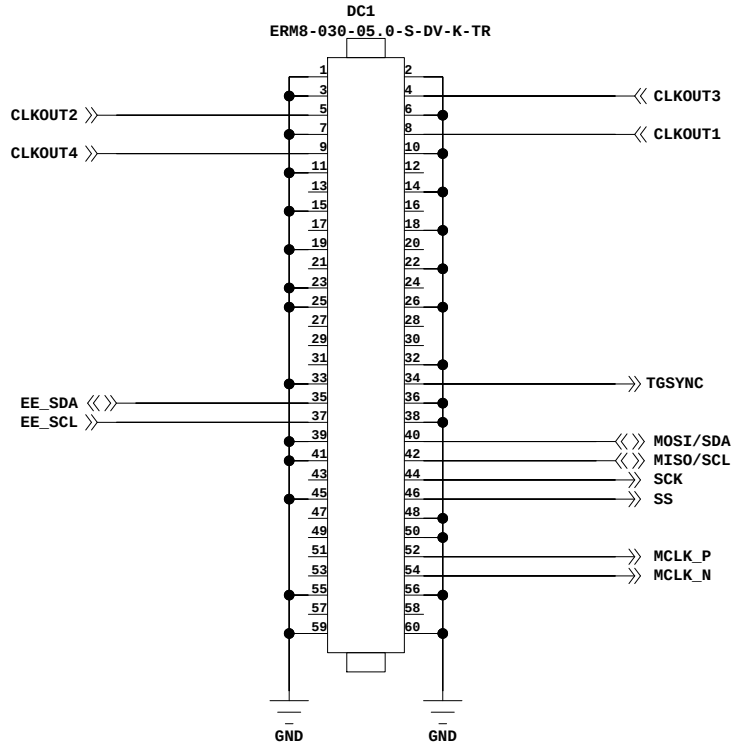
- A0 - Initial Revision
- B0 - Silkscreen updates no schematic changes.
- B1 - Updating device symbol to Rev B, no PCB change





Layout Notes
See Layout Document
CDB82L44 Schematic and Layout Guidelines

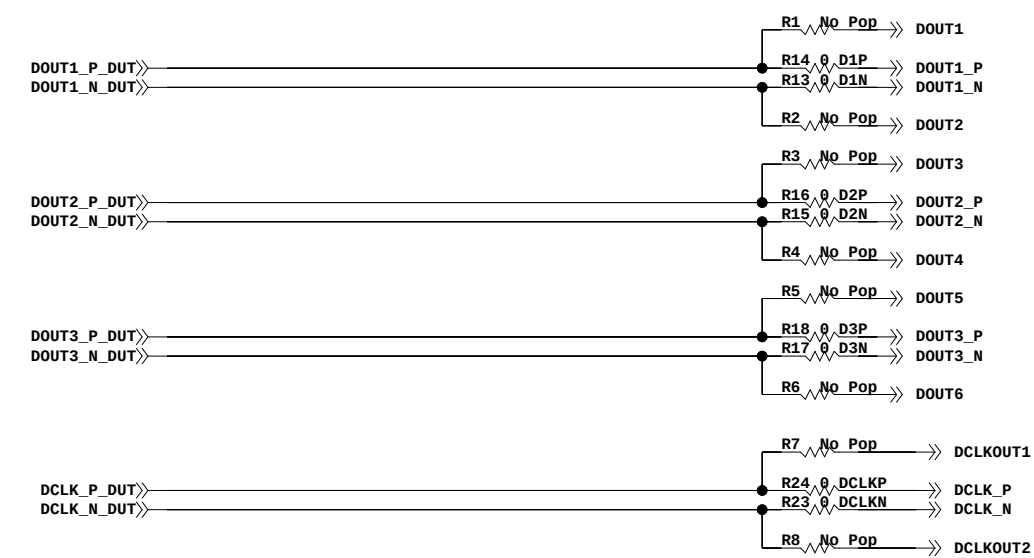




Layout Notes
See Layout Document
CDB82L44 Schematic and Layout Guidelines



CMOS/LVDS Termination and Population Options

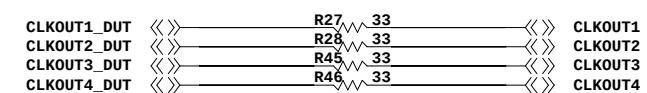


Note 3

Note 4

Note 5

CLKOUTx Termination

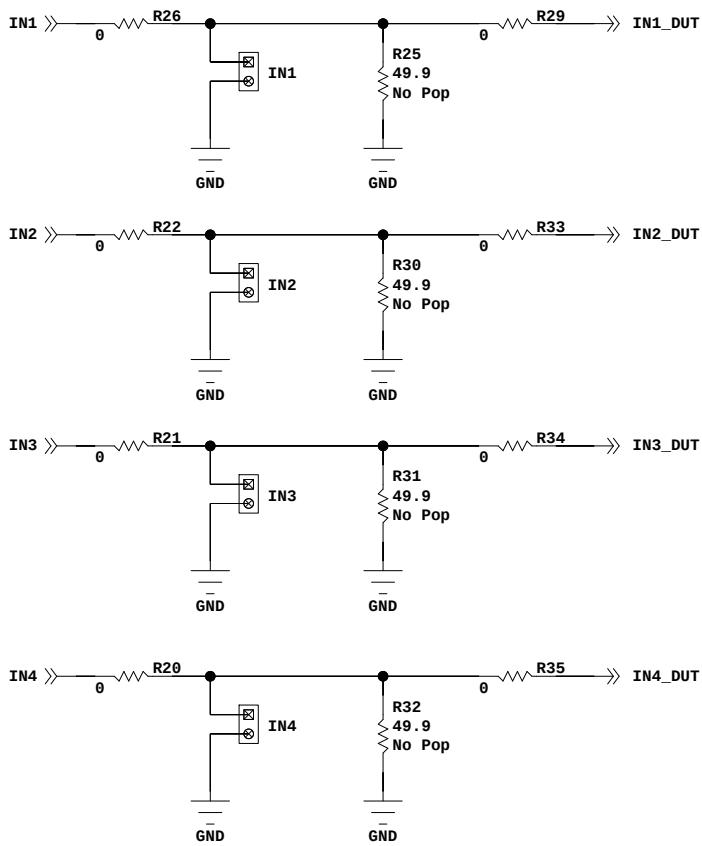


Note 2

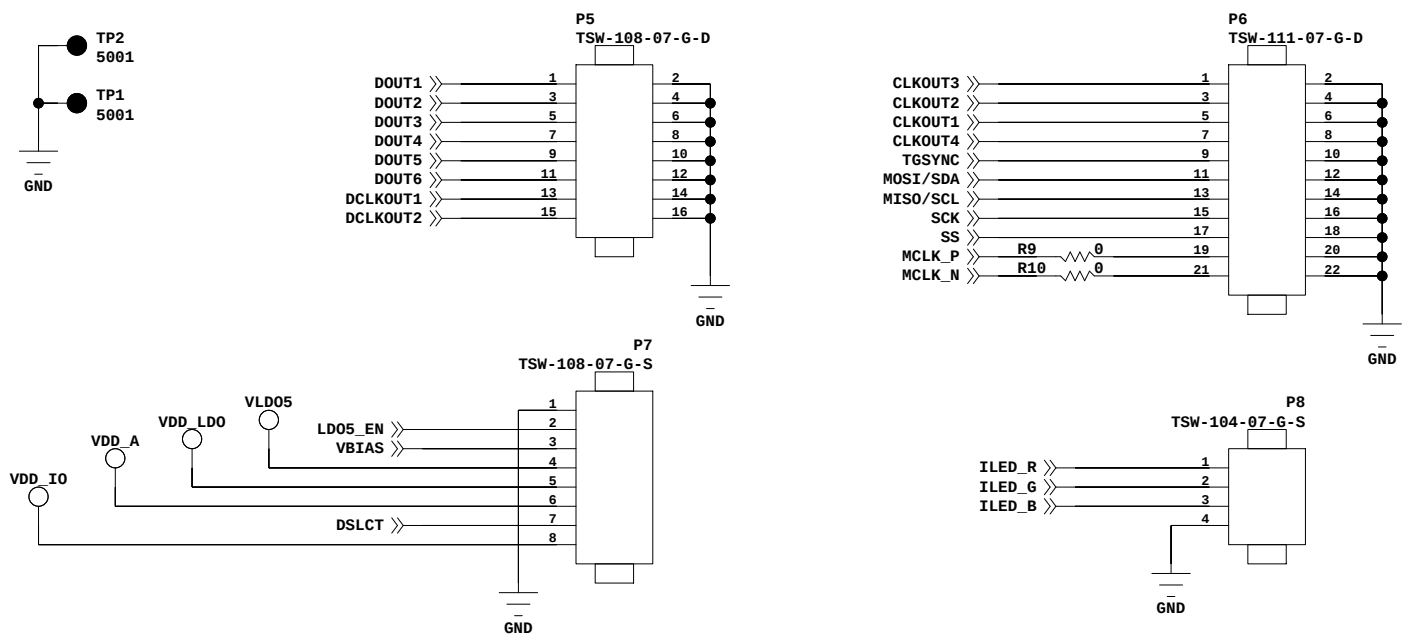
Layout Notes
See Layout Document
CDB82L44 Schematic and Layout Guidelines



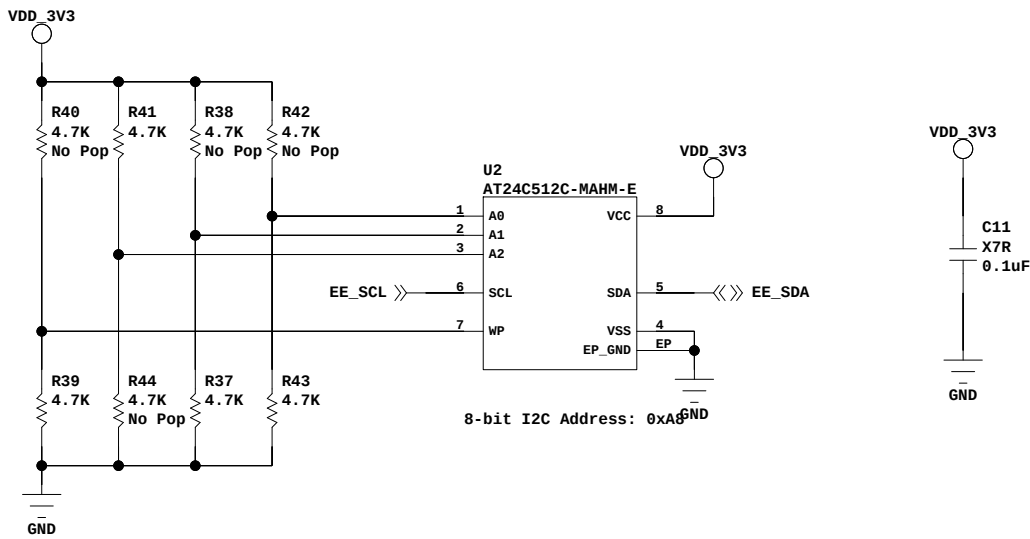
Input



Test Points



Board ID EEPROM



Layout Notes
See Layout Document
CDB82L44 Schematic and Layout Guidelines

