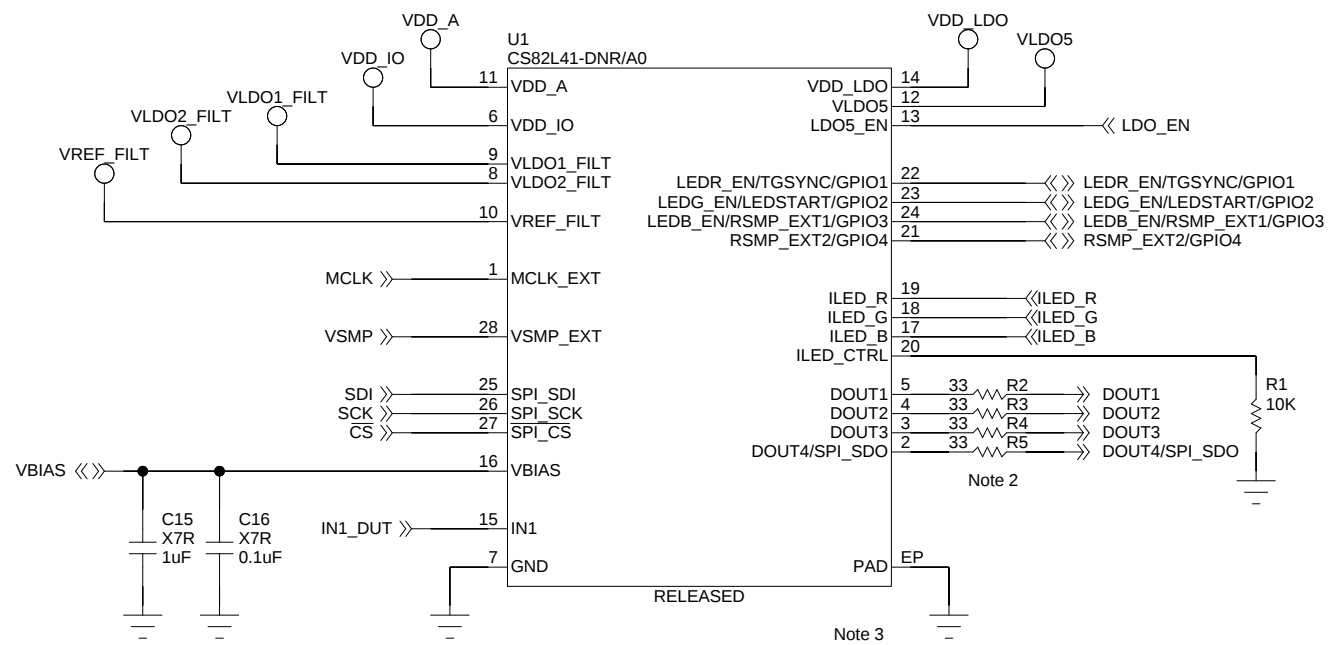
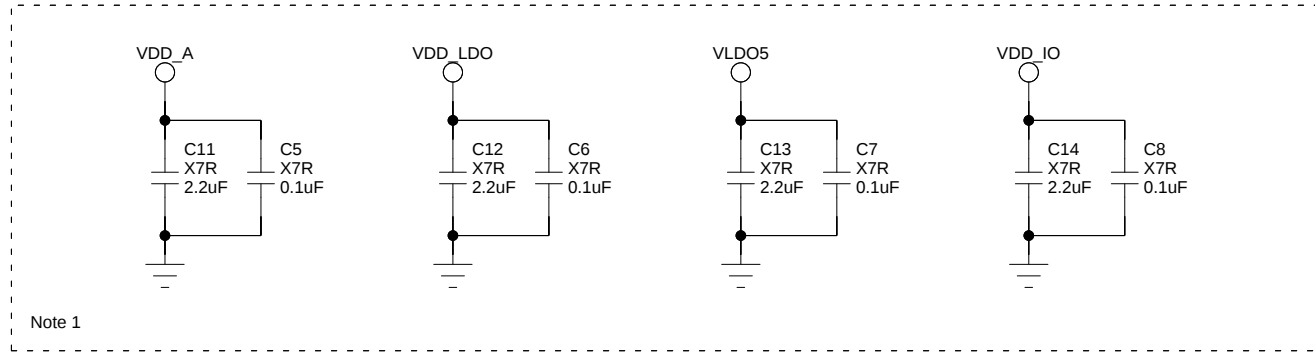
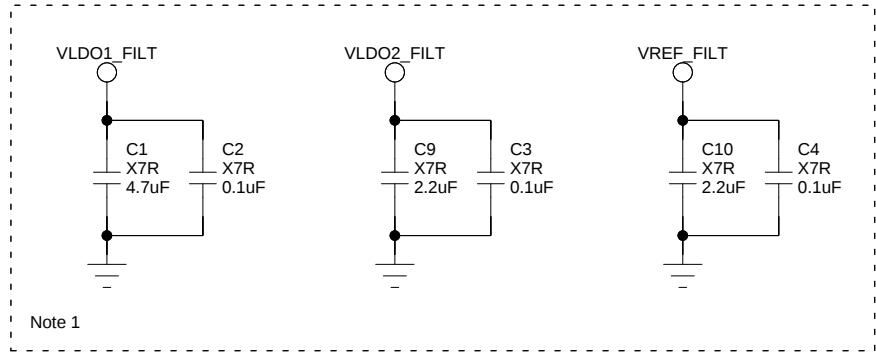


CDB82L41-DC

REV HISTORY:
A0 - Original Rev

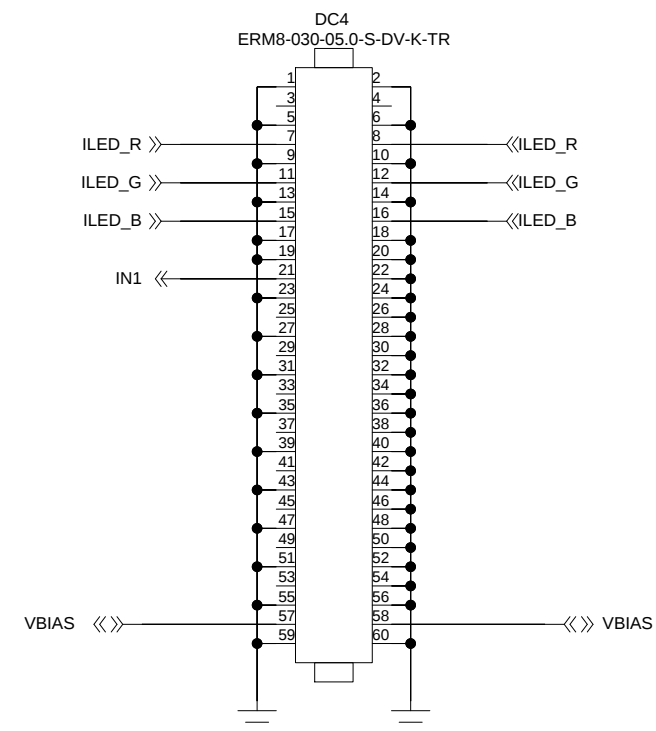
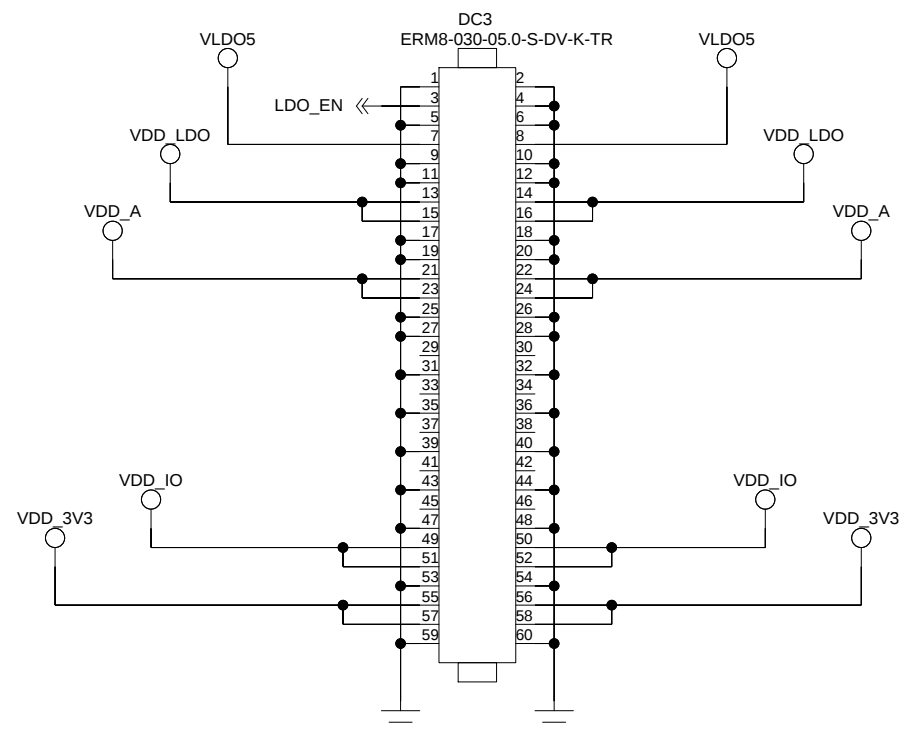
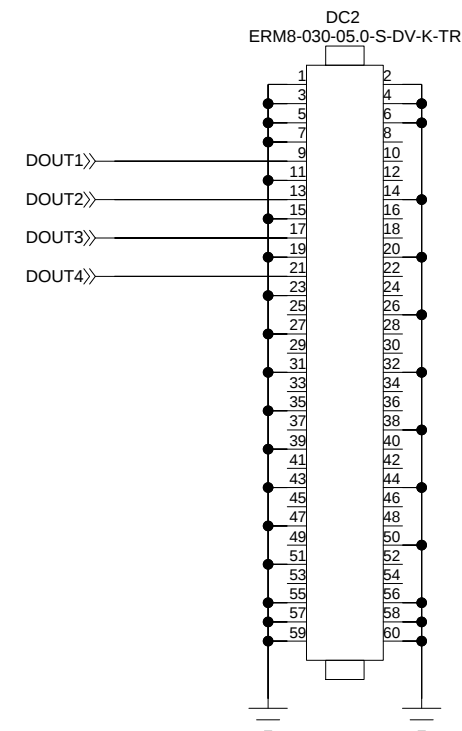
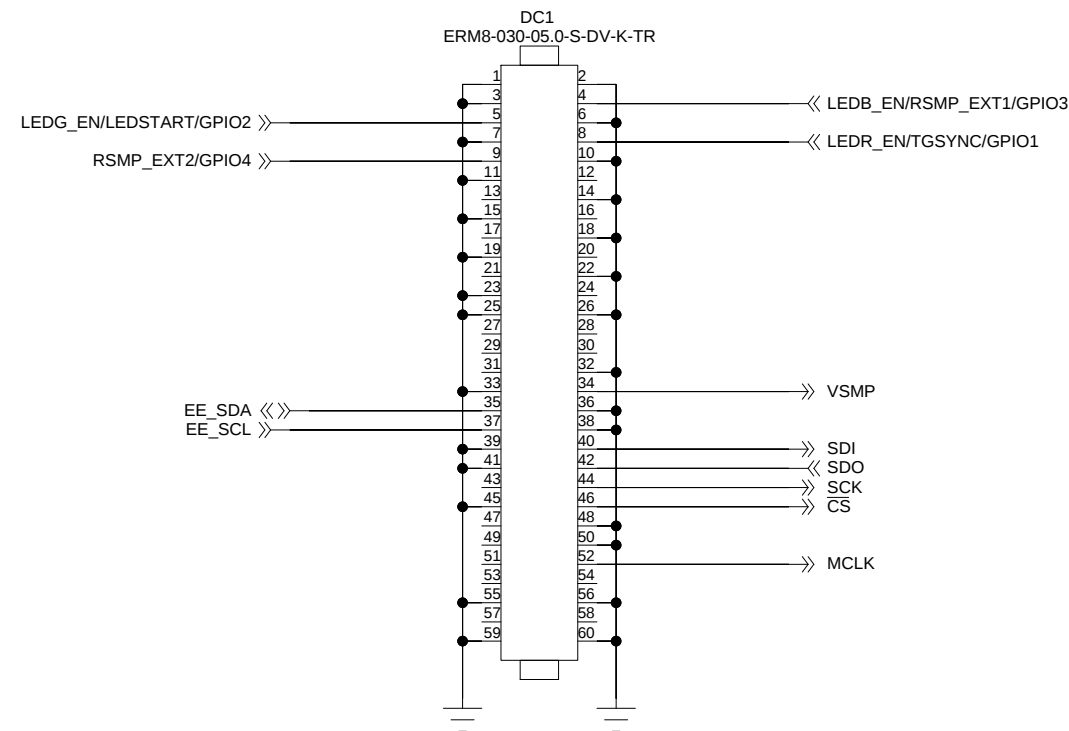




Layout Notes

See Layout Document
CS82L41 Schematic and Layout Guidelines

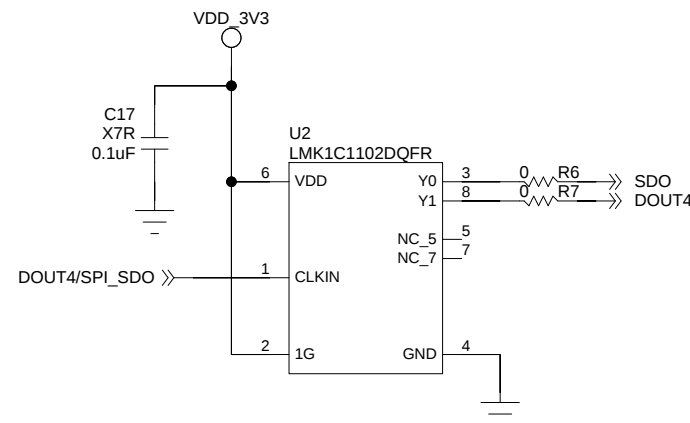




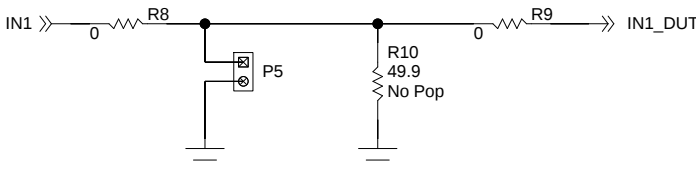
Layout Notes
See Layout Document
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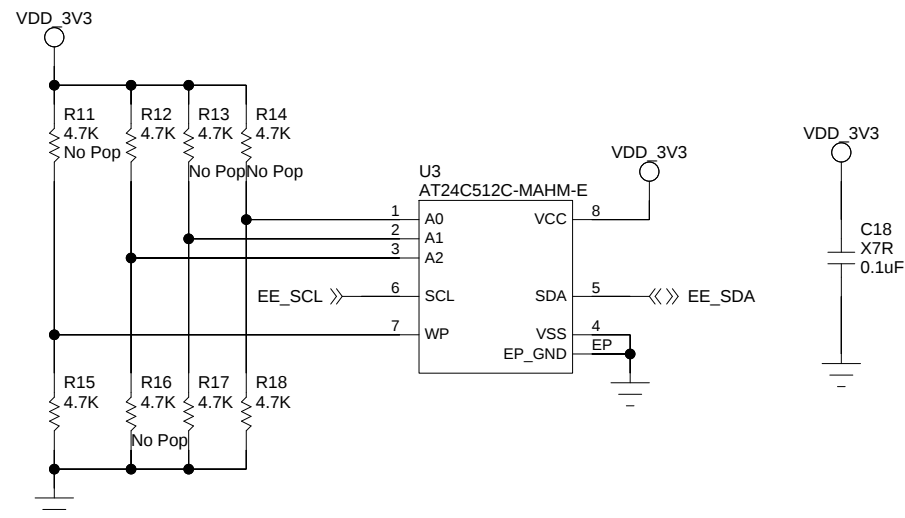
SPI_SDO/DOUT4 Fanout



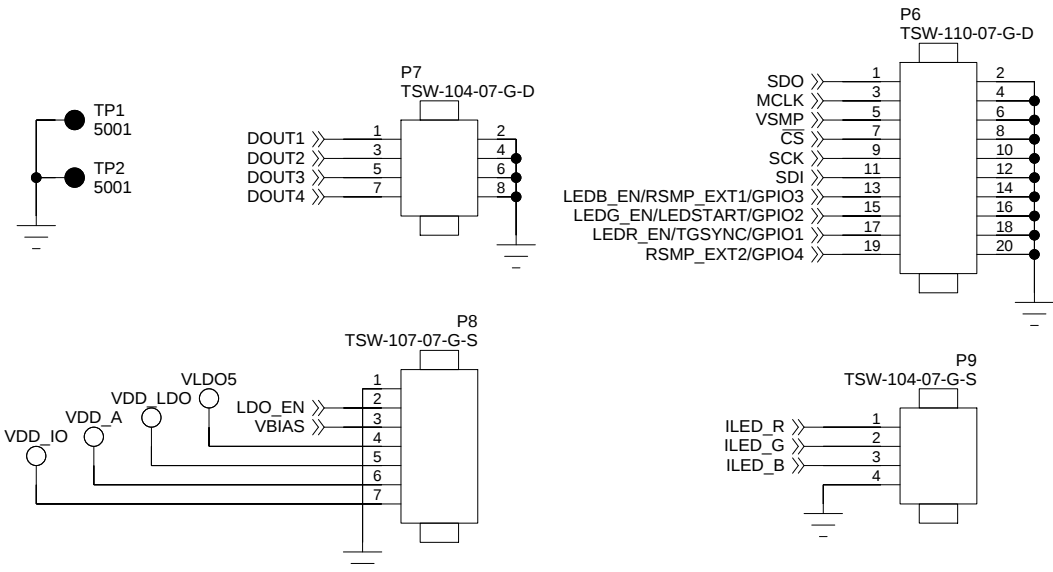
Input



Board ID EEPROM



Test Points



Layout Notes

See Layout Document
CS82L41 Schematic and Layout Guidelines

