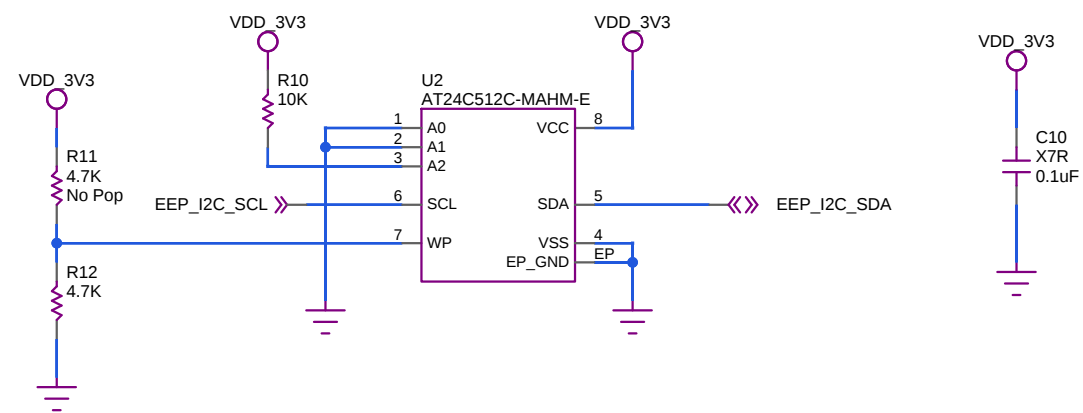


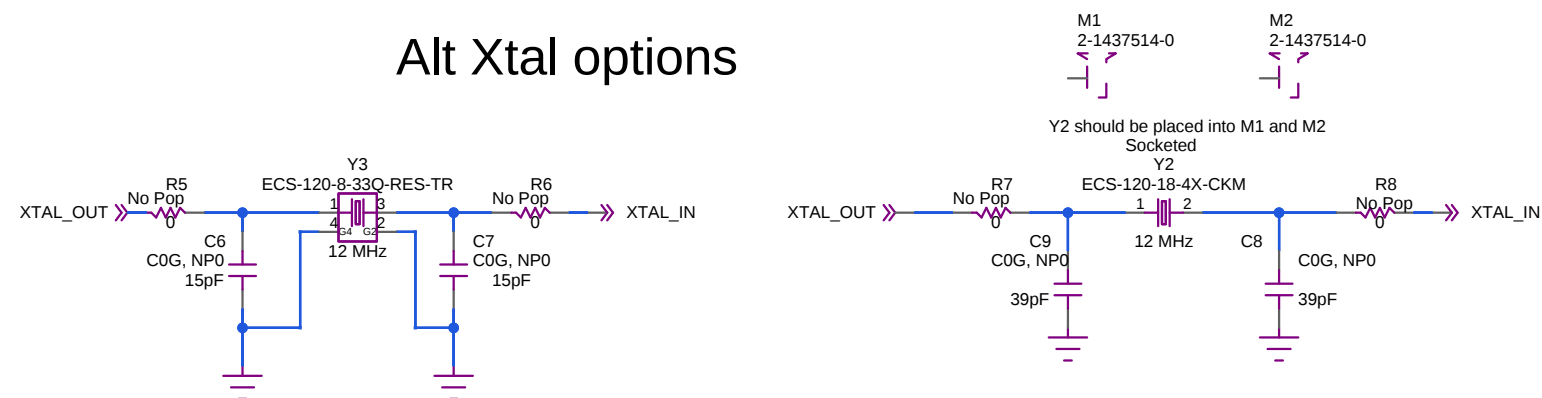
CDB2600-DC-SD-REV-A

DEVICE: CS2600

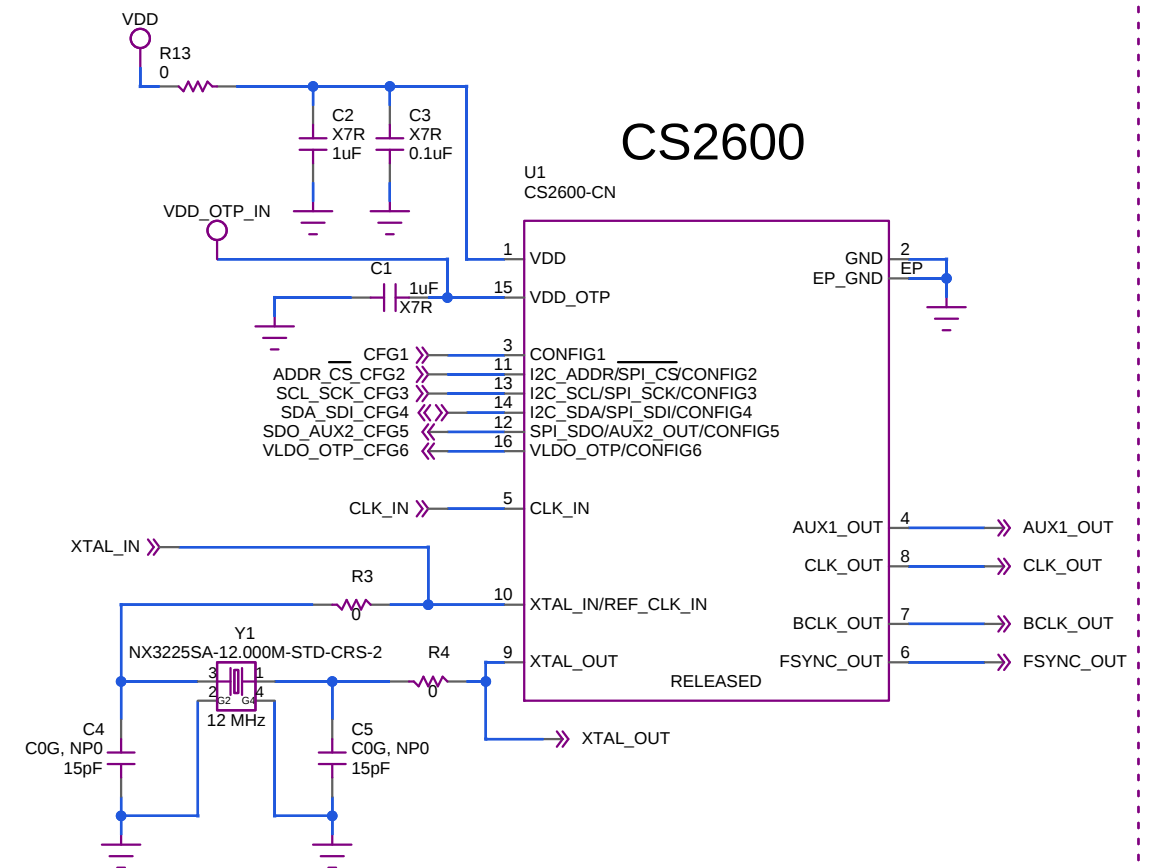
Board ID EEPROM



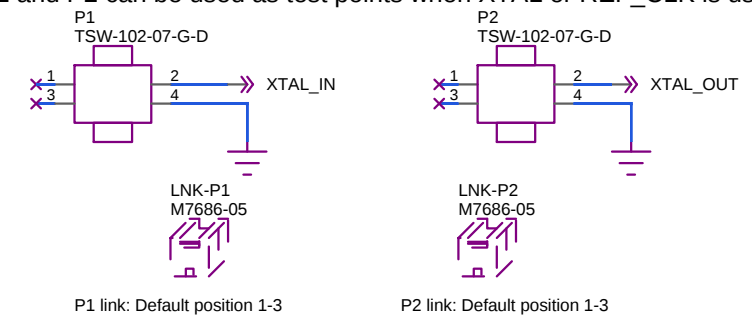
Alt Xtal options



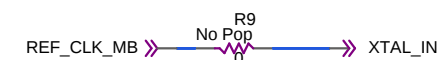
CS2600



To select internal LCO of CS2600: Move jumpers P1 and P2 across 2-4
P1 and P2 can be used as test points when XTAL or REF_CLK is used



Pop R9 and unpop R3, R4 for external REF_CLK



VDD_OTP and VLDO_OTP_OUT select

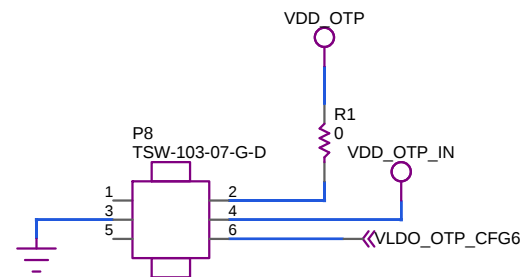
C: No OTP Programming (Default): P8: Jumper across pins 3 and 4

B: VDD_OTP (2.5V) is required only while programming OTP memory when VDD = 1.8V: P8: Jumper across pins 2 and 4

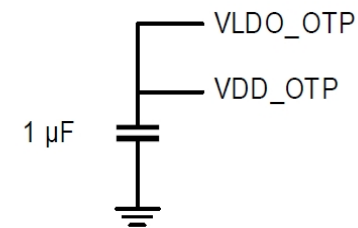
A: OTP Programming when VDD = 3.3V: P8: Jumper across pins 4 and 6



P8 link: Default position 3-4

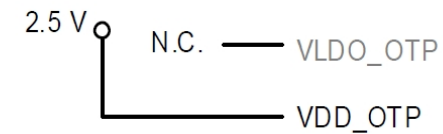


(A) OTP programming ($VDD = 3.3\text{ V}^1$)



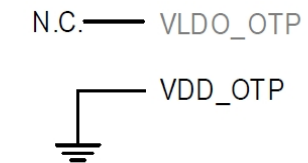
1. The impedance between the VLDO_OTP and VDD_OTP connection should be minimized.

(B) OTP programming ($VDD = 1.8\text{ V}^2$)



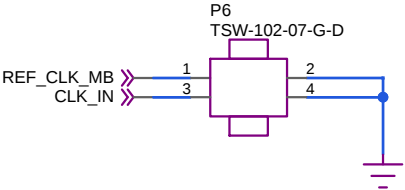
2. The external power supply of 2.5 V is required only while programming the OTP memory.

(C) No OTP programming

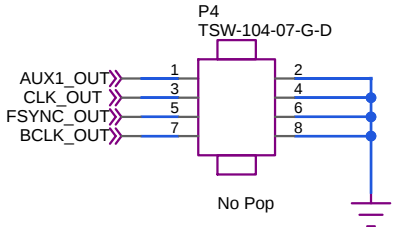


Test points

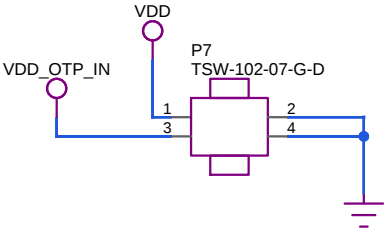
Input Clocks



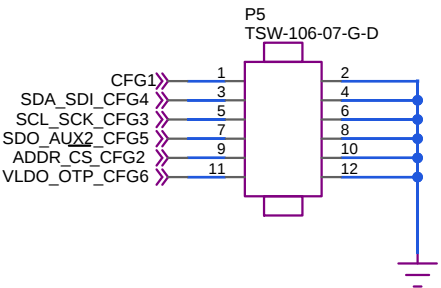
Output Clocks



VDD and VDD_OTP supplies



SPI/I2C/CONFIG



Connectors to/from mother board

